

Fig 1

Transmit 201

Receive 202

FOR TEST SET

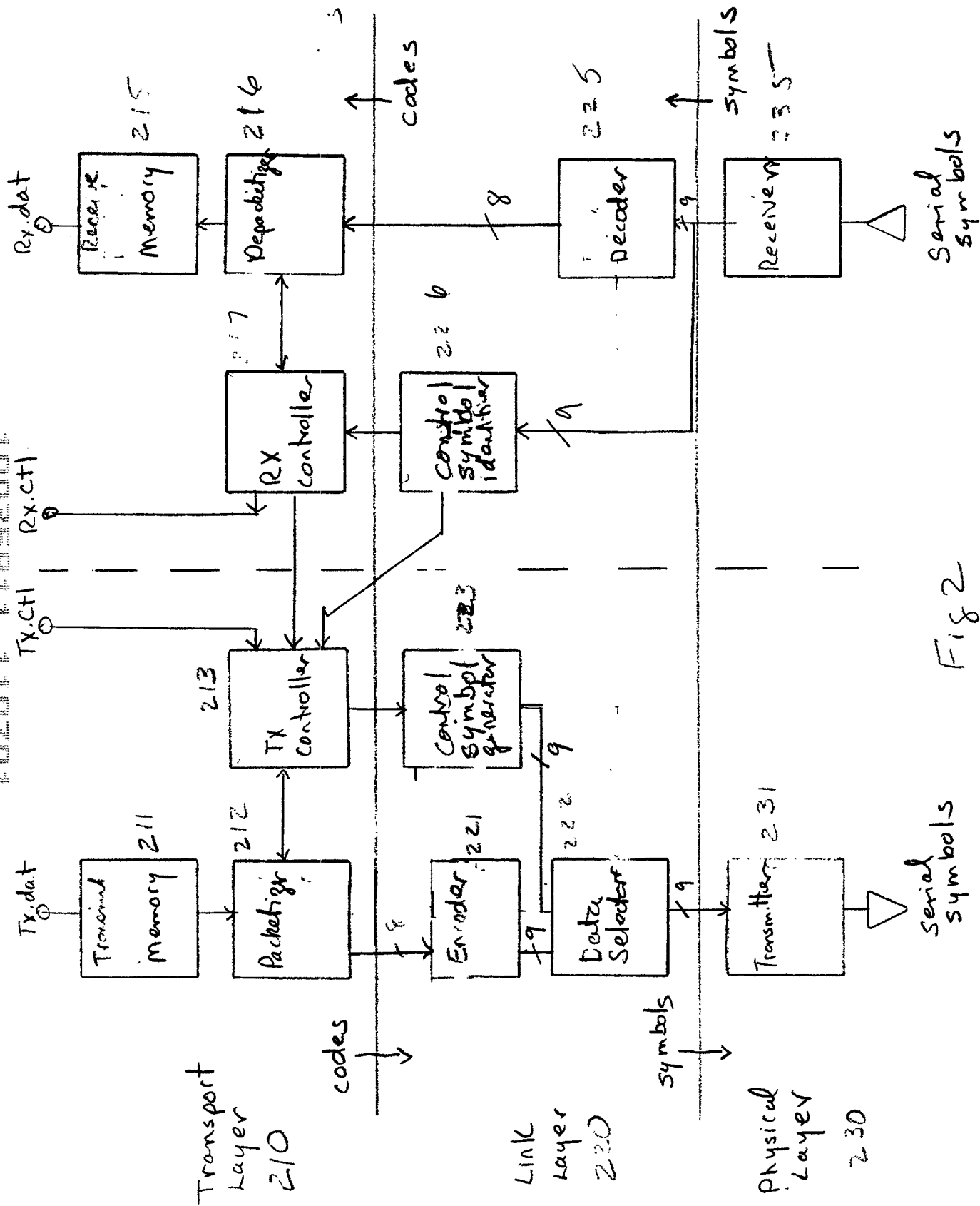


Fig 2

Physical Layer 230

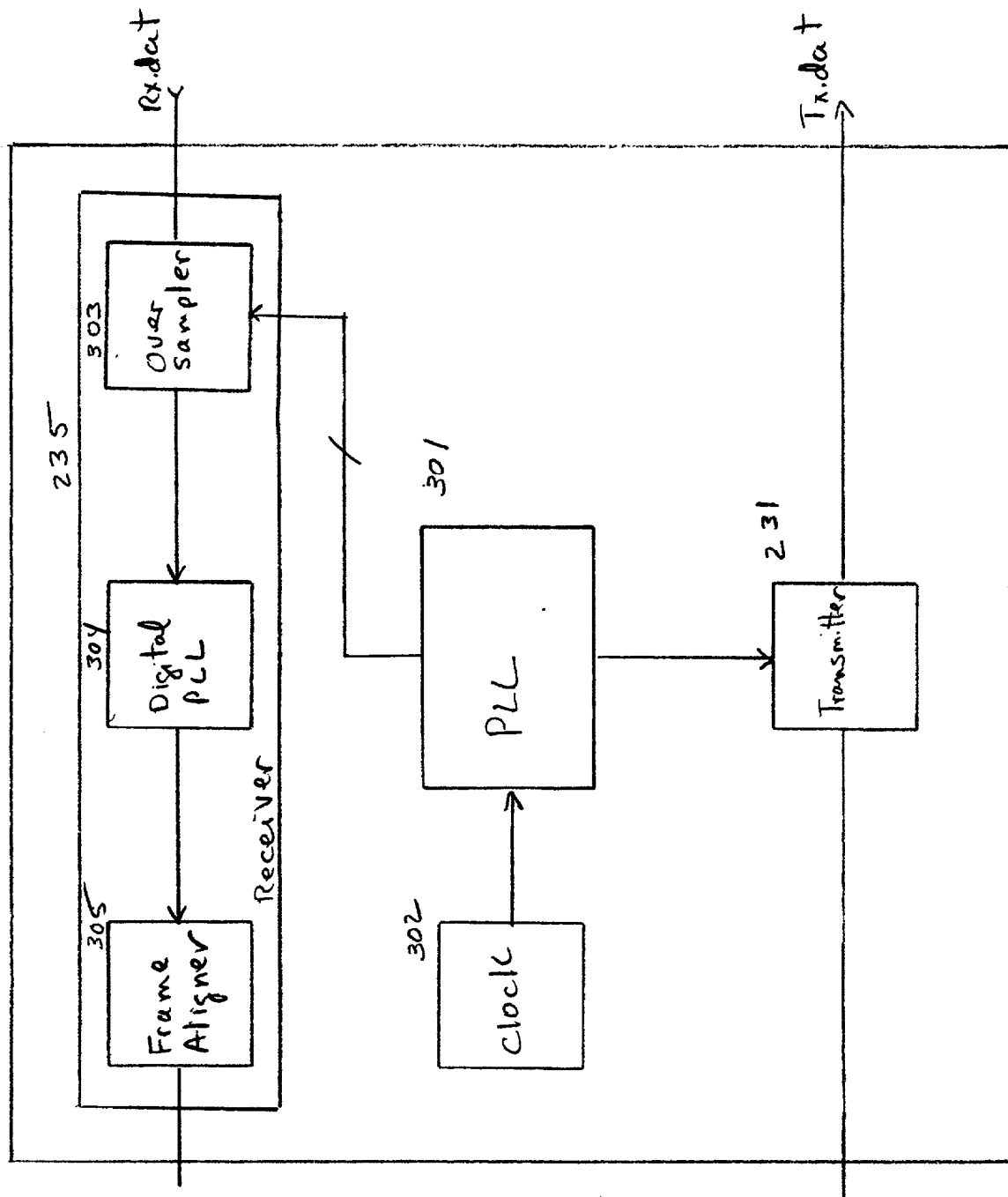


Fig 3

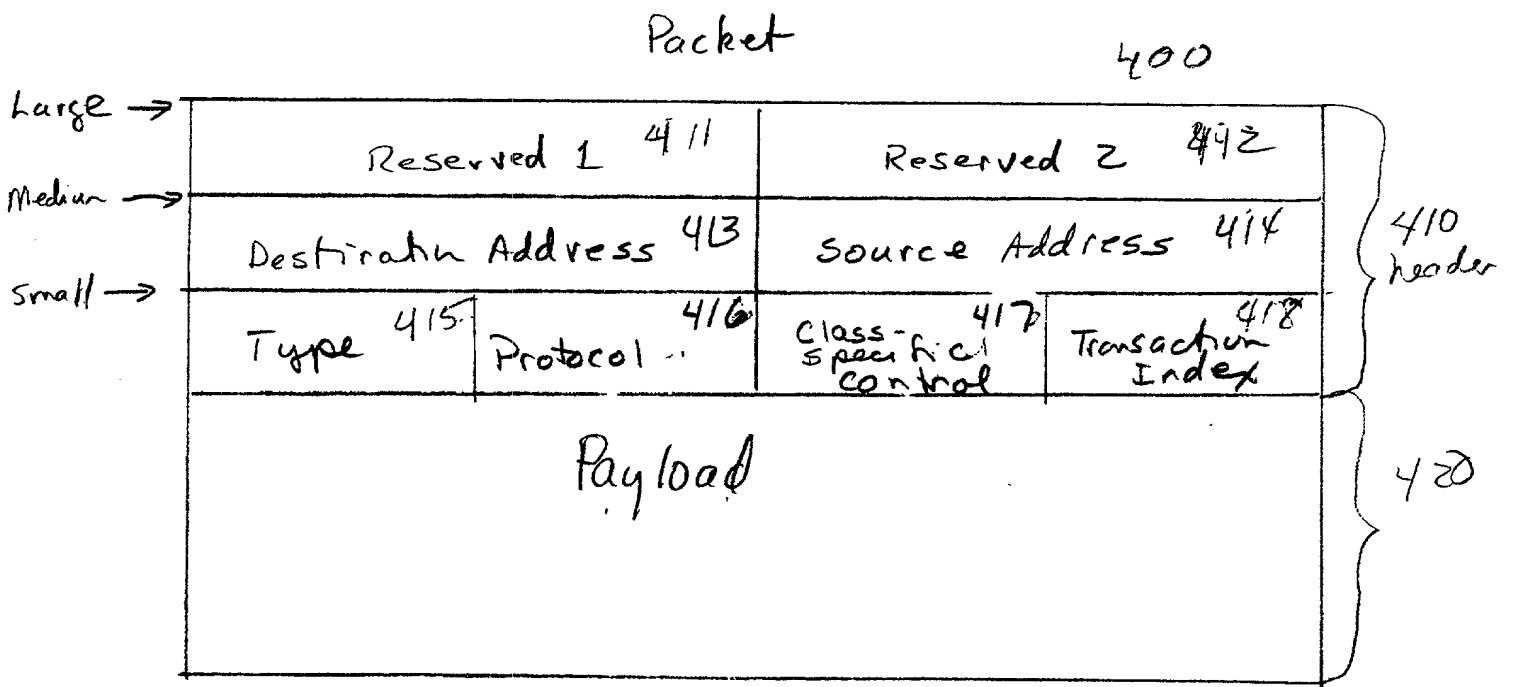
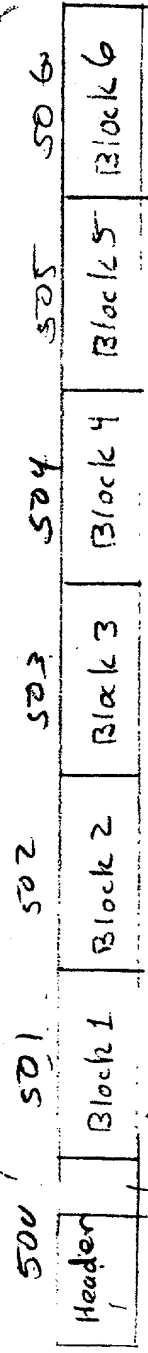
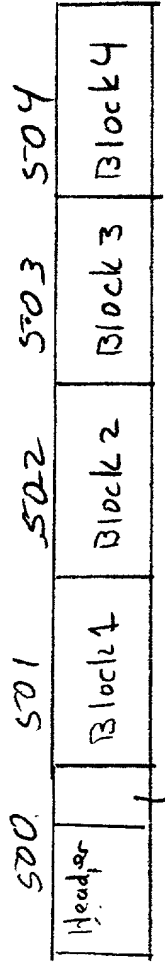


Fig 4

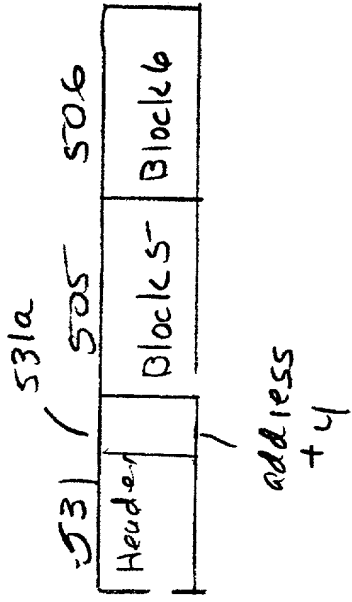
payload 511
 TO OFF THE FOOT



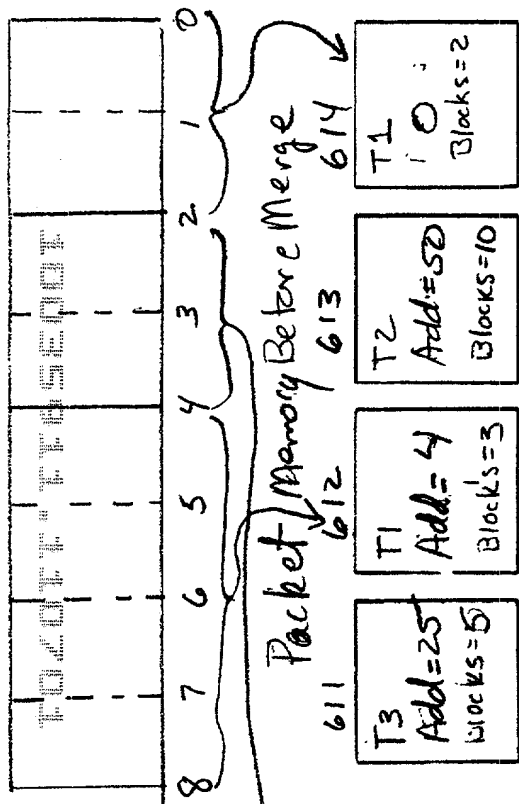
570



520



F.45



Received Packet

T1

Add = 2

Blocks = 2

630

Packet Memory After Merge

620

611

T3

Add = 25

Blocks = 5

612

T1

Add = 4

Blocks = 3

613

T2

Add = 50

Blocks = 10

614

T1

Add = 0

Blocks = 2

F. 86

T. 1

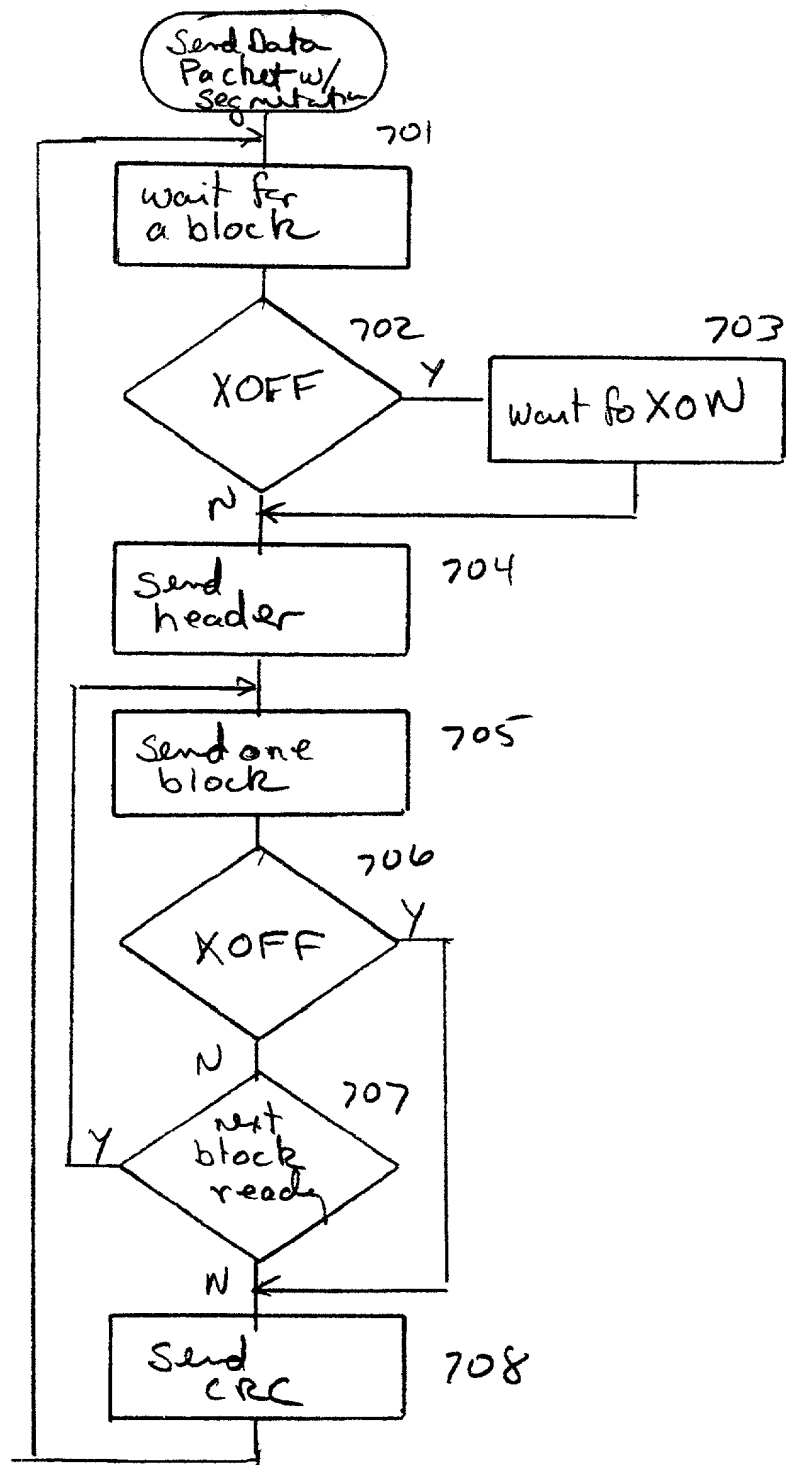


Fig 7

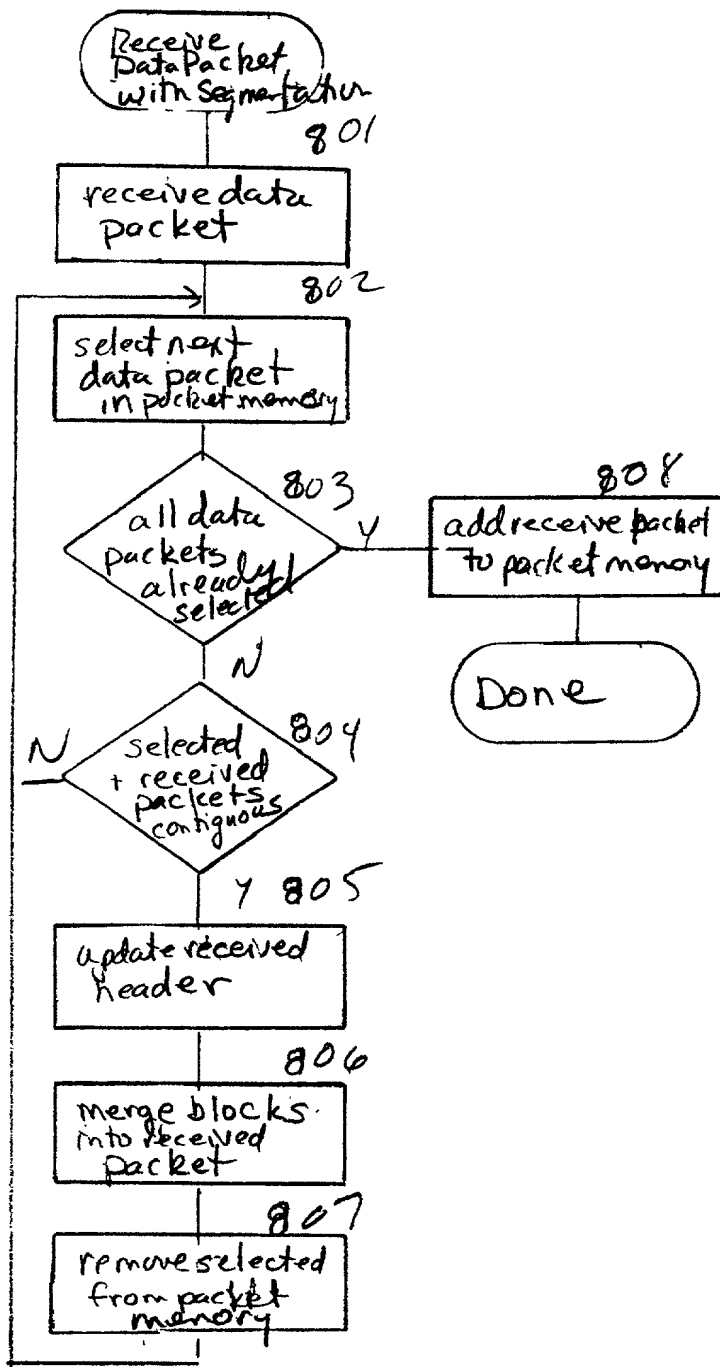


Fig 8



Sync + packet type

Fig 9A

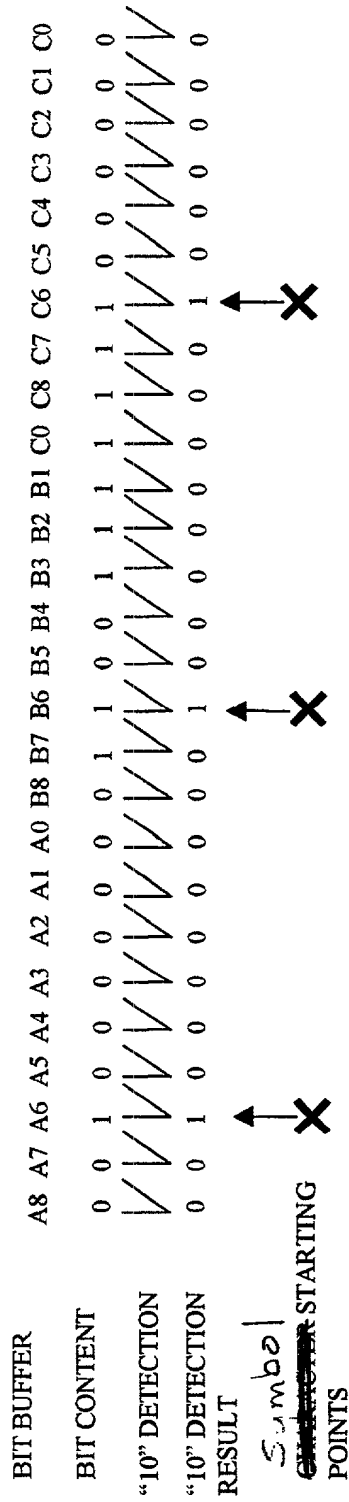


FIG.10

Fig 9B

910

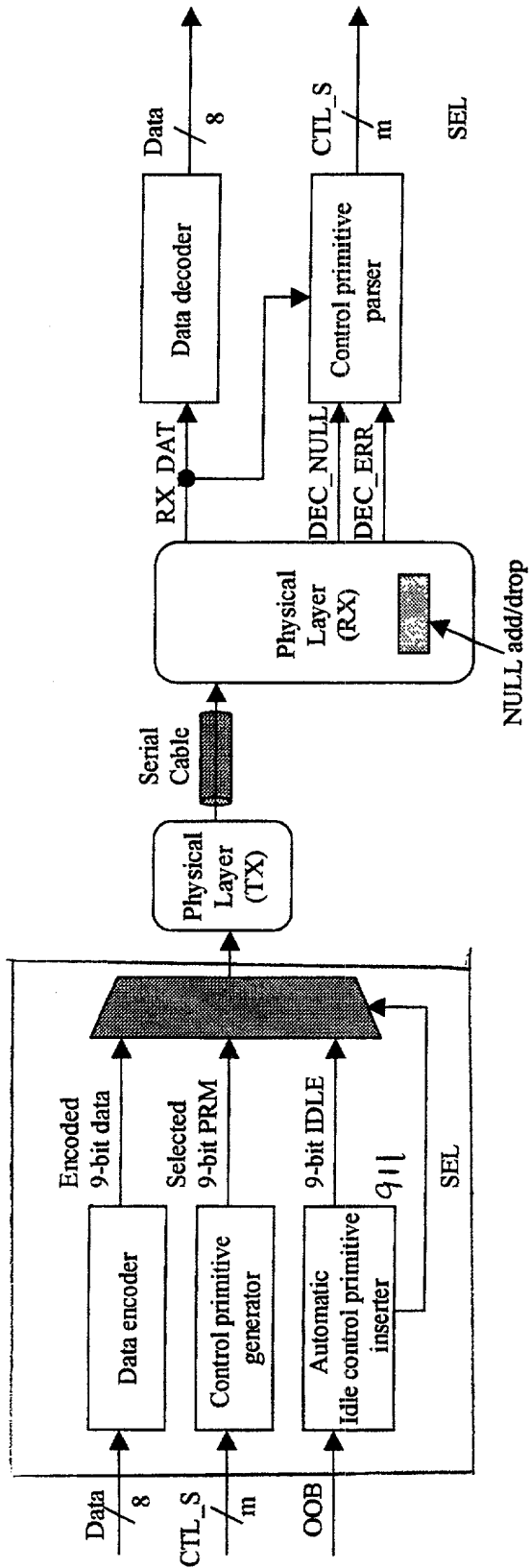


Fig. 9C

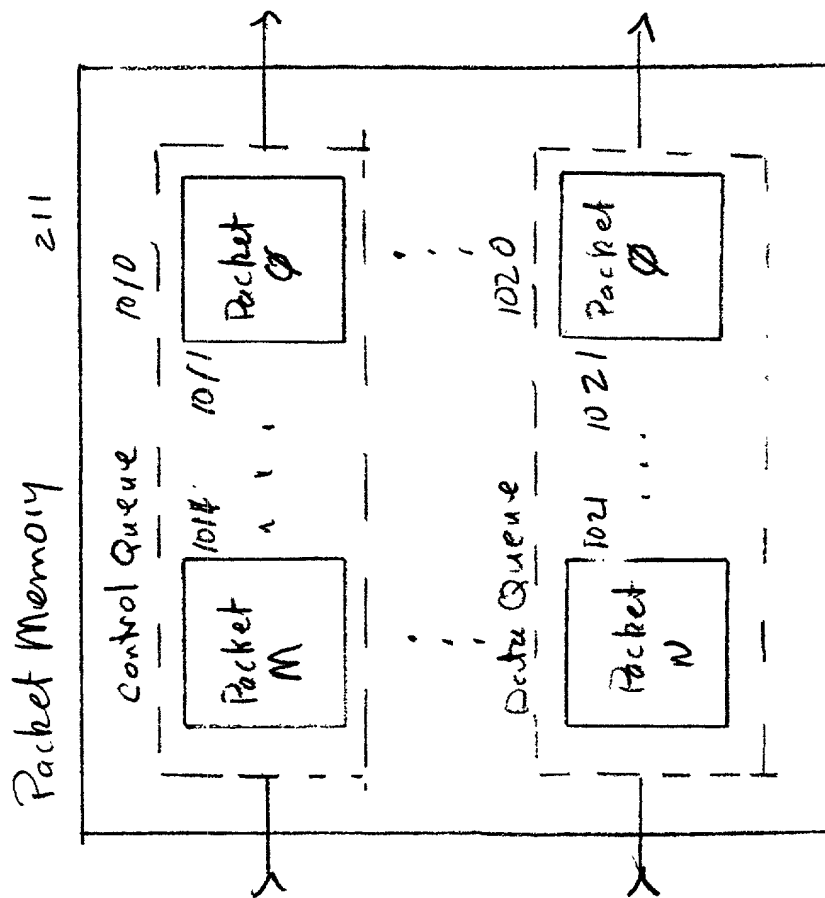


Fig 10

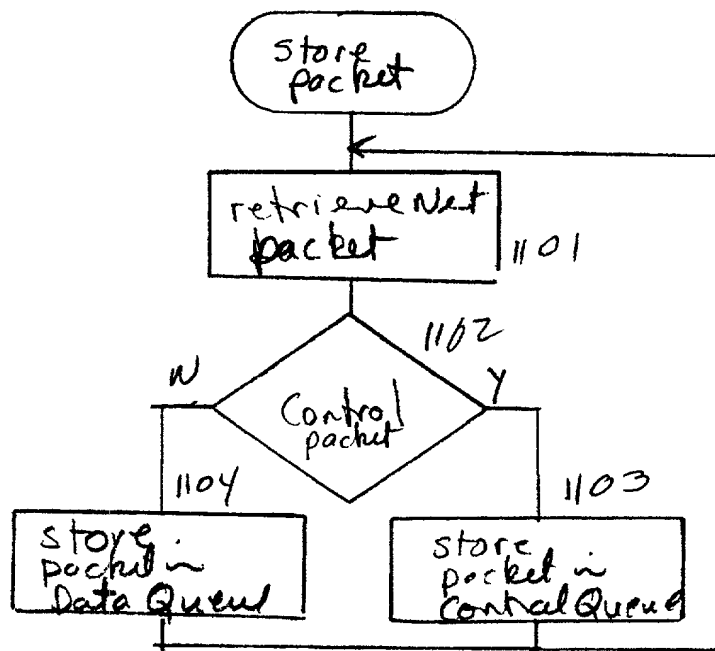


Fig 11

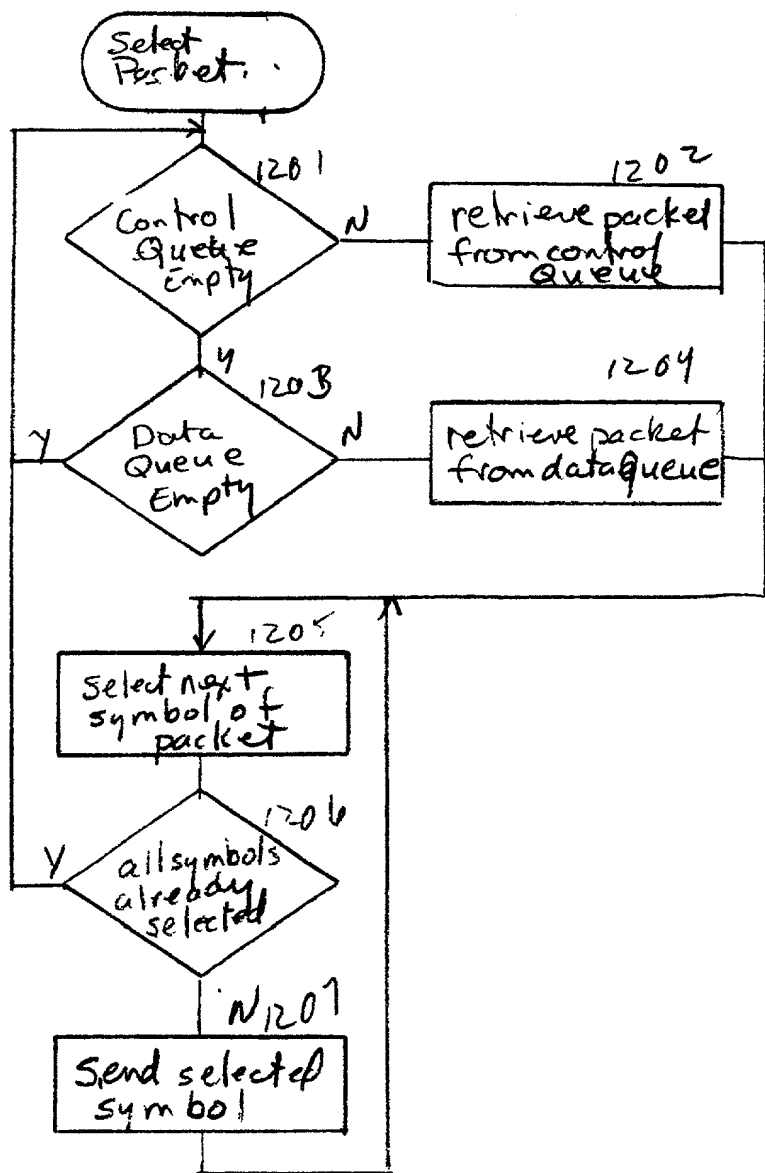


Fig 12

TD/DTT TEST

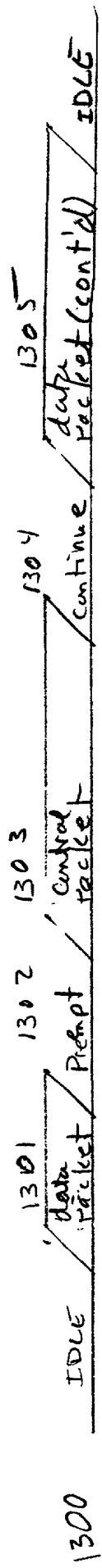


Fig 13

10035911.110701

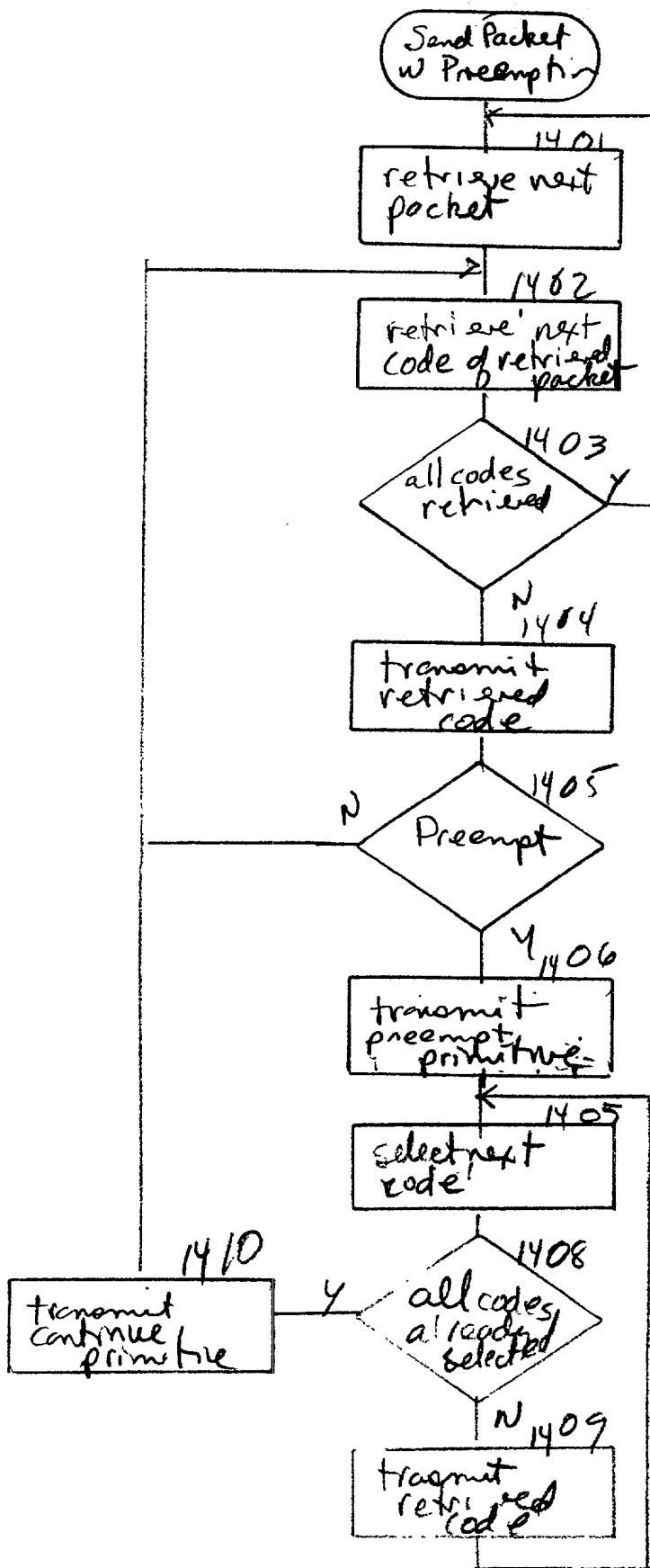


Fig 14

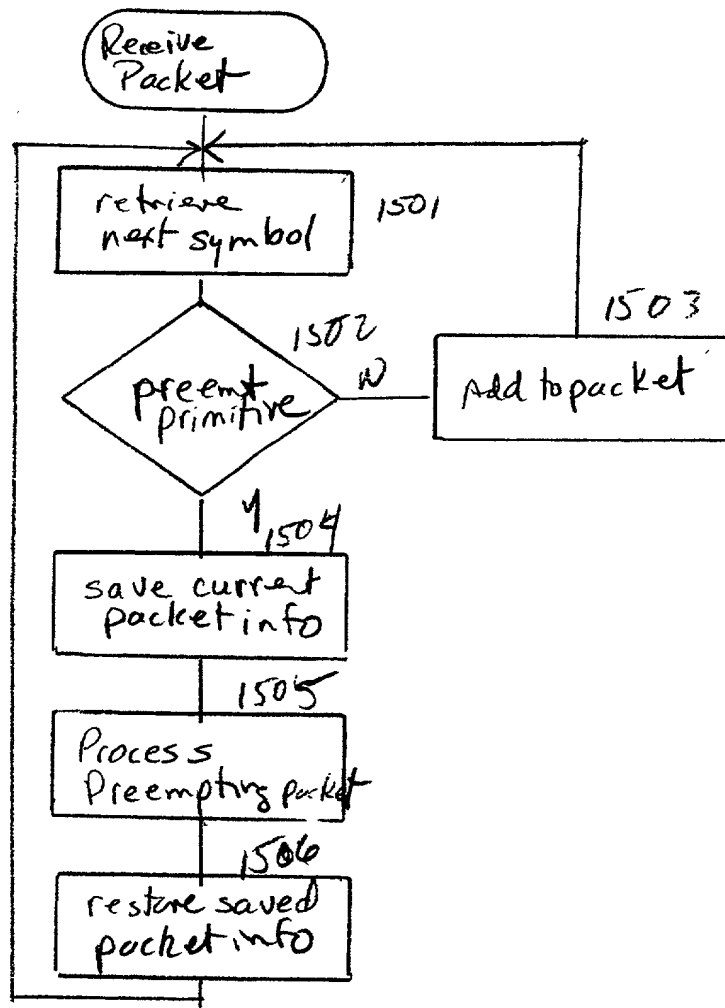


Fig 15

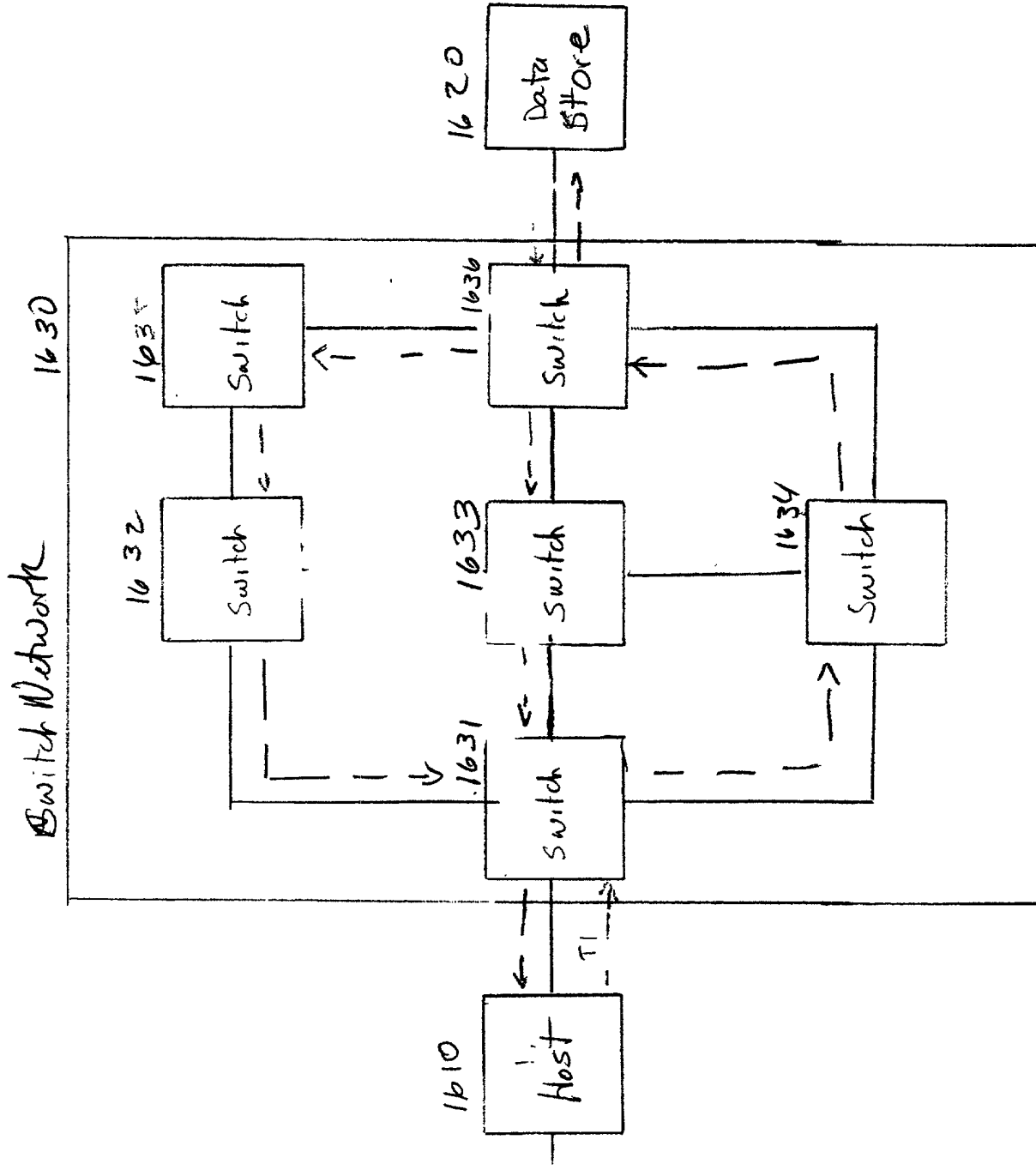
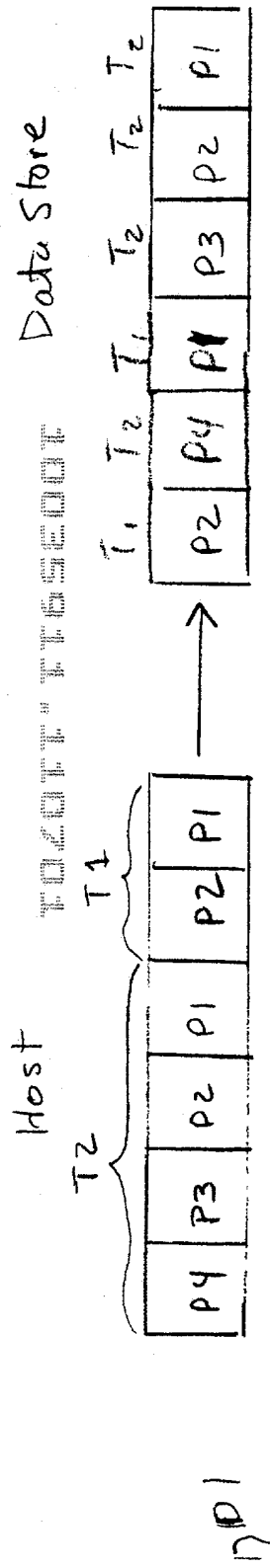
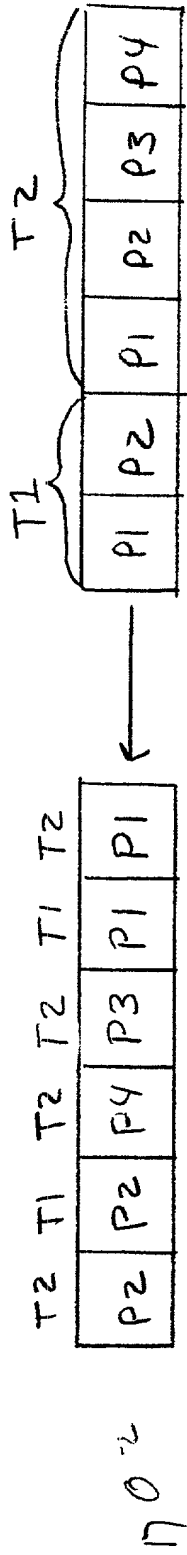


Fig 16



Preserving Packet Order w/ Transaction



No Packet or Transaction Ordering

Fig 17

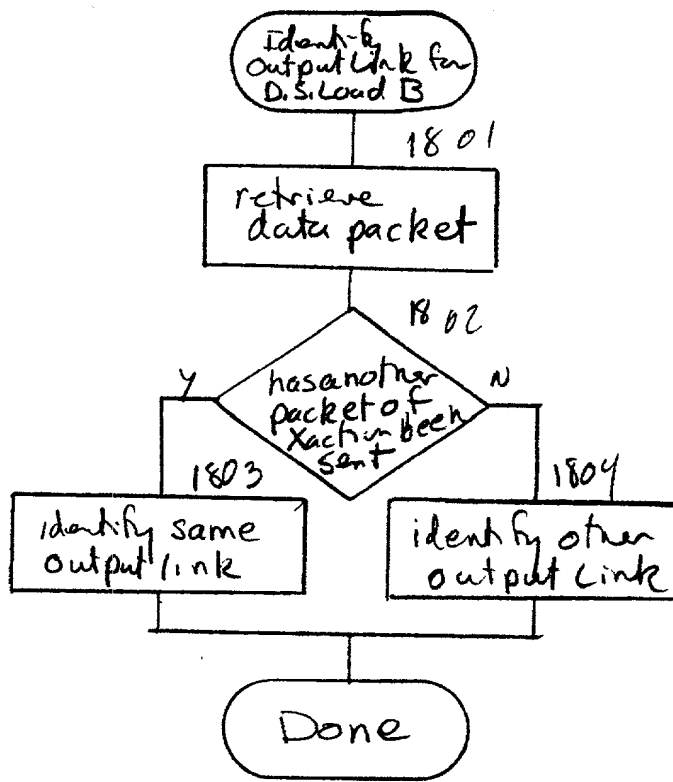


Fig 18

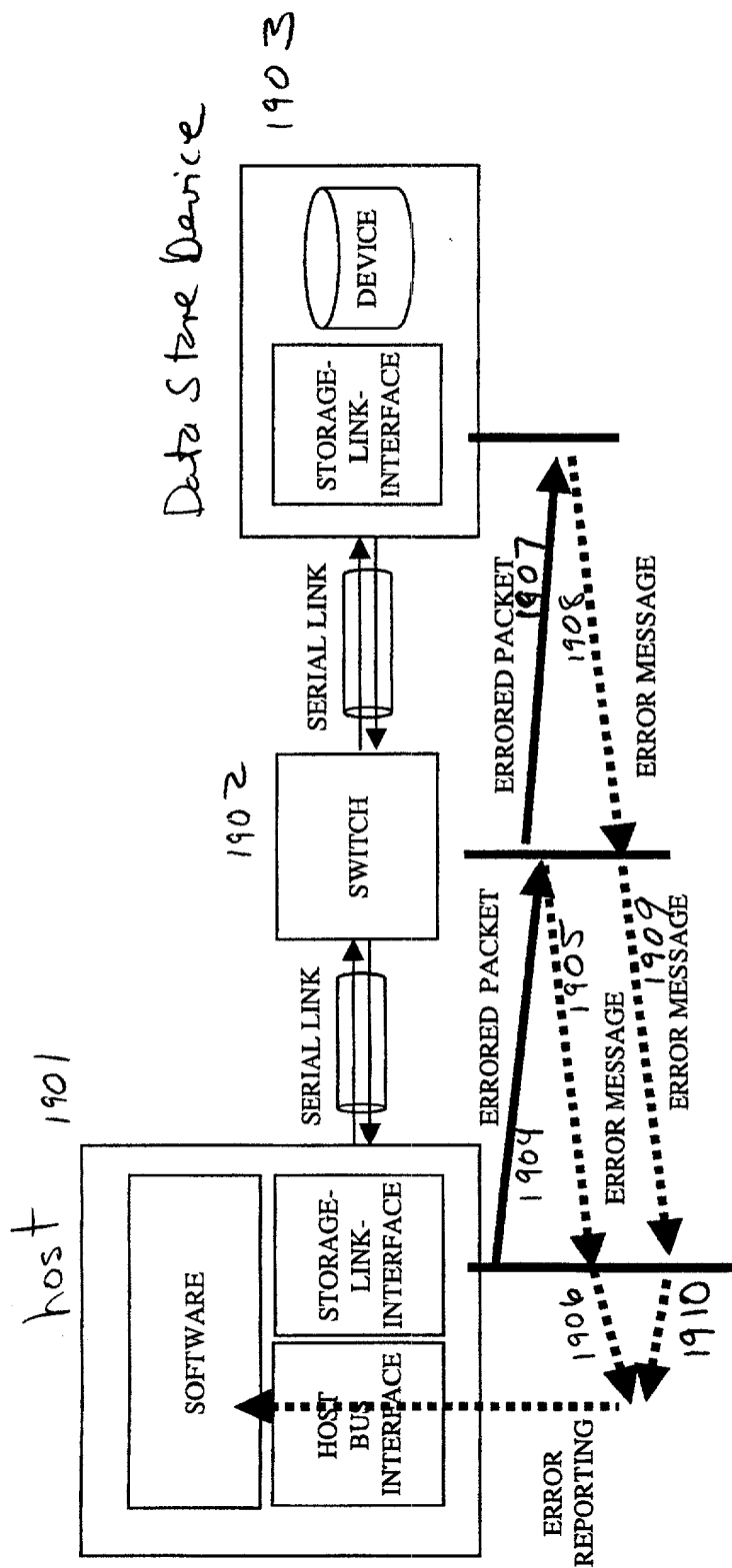


Fig 19A

1901

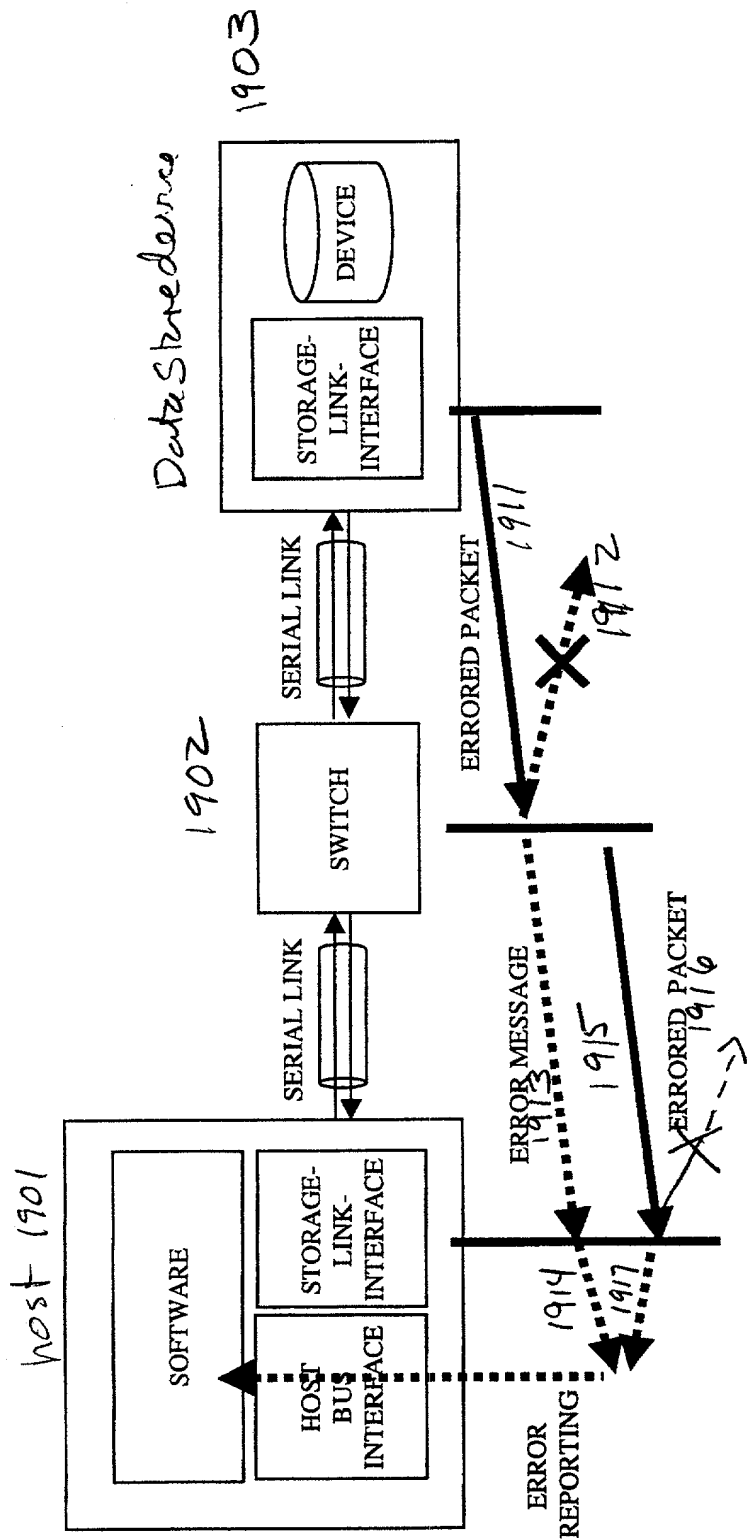
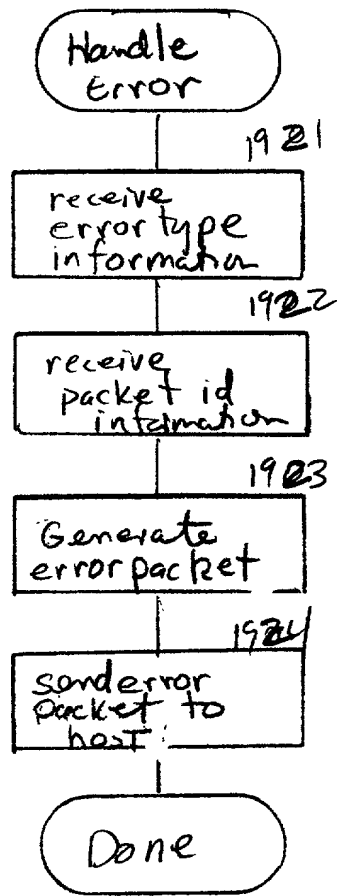


Fig 19B



19C

| 8b Code | 9 bit symbol |
|-----------|--------------|
| 0000 0000 | 101010101 |
| 0000 0001 | 101010100 |
| 0000 0010 | 101010111 |
| ⋮ | |
| 0101 0101 | 001010101 |
| ⋮ | |
| 0111 0110 | 001110110 |
| 0111 0111 | 100100010 |
| ⋮ | |
| 1111 1111 | 110101010 |

Fig 20

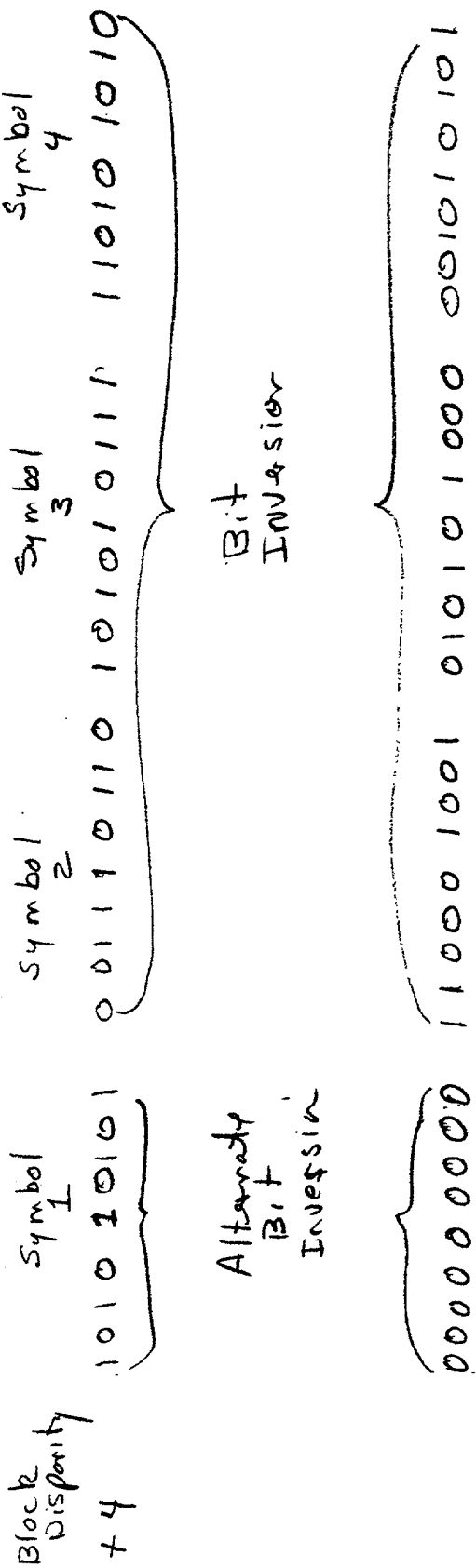


Fig 21A

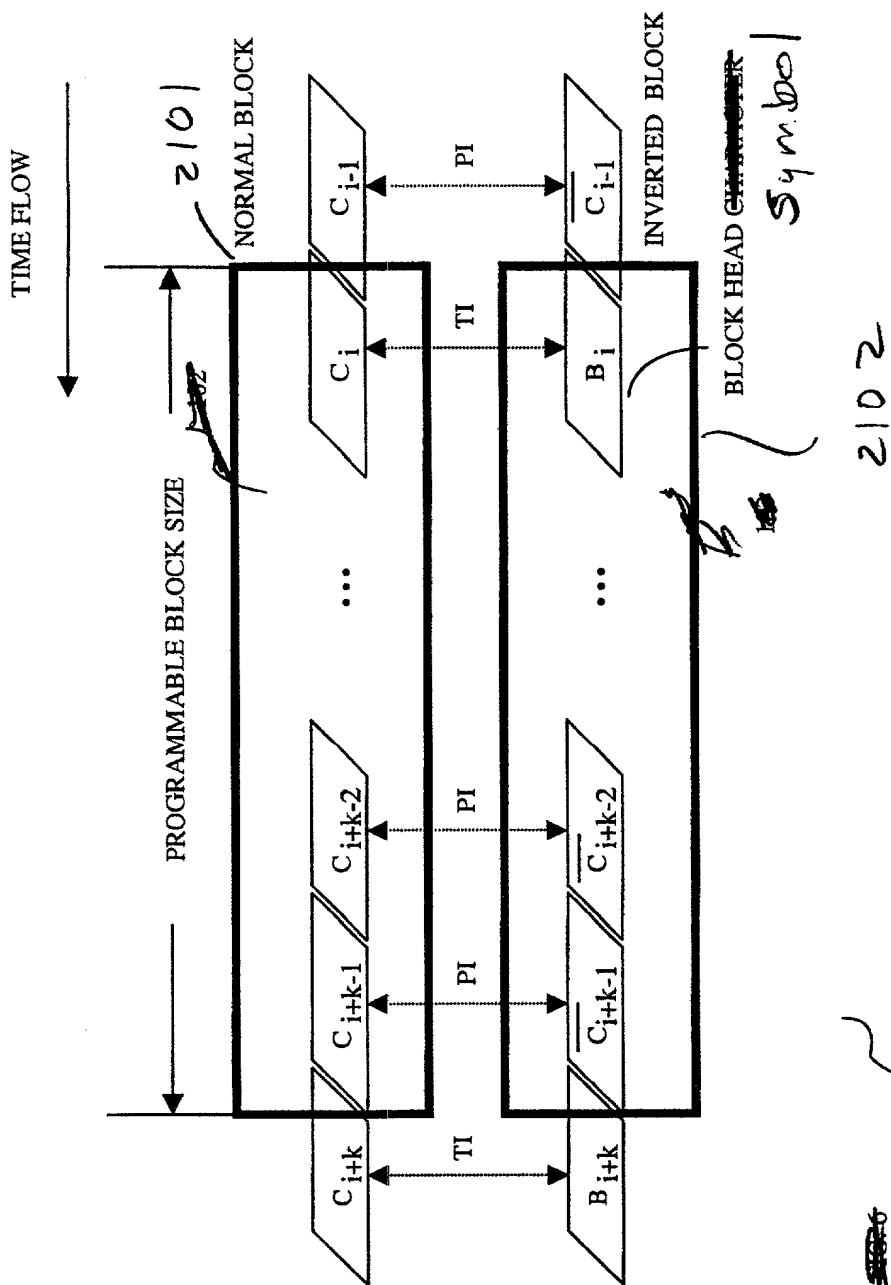
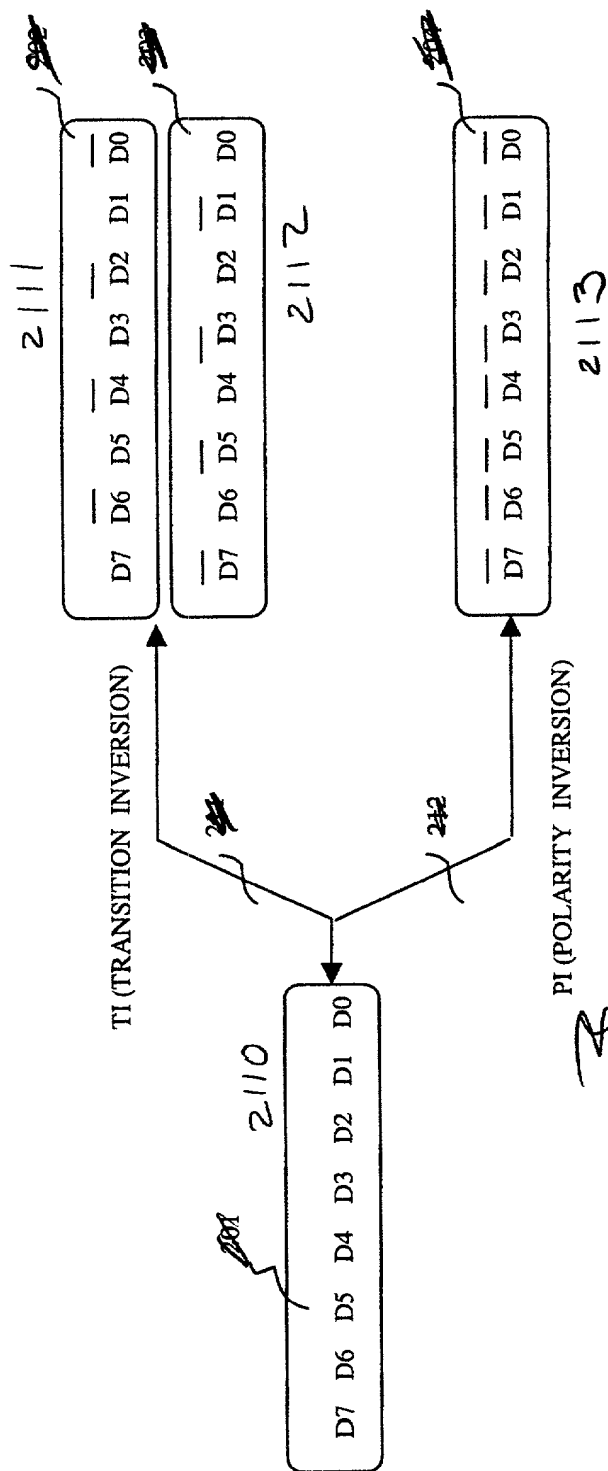


Fig 21B



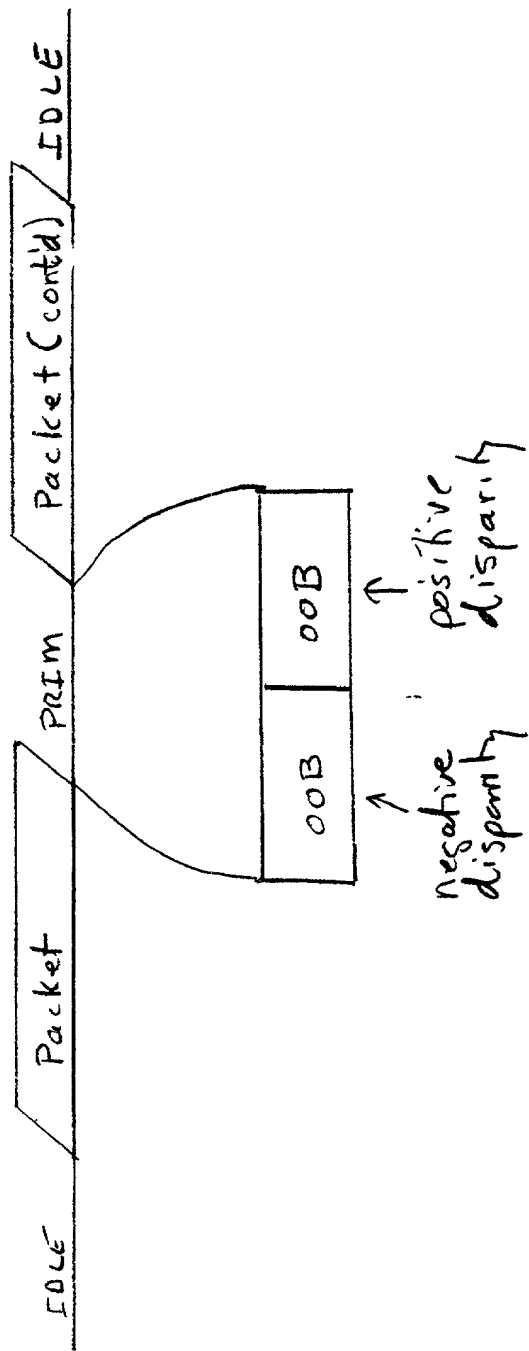


Fig 22

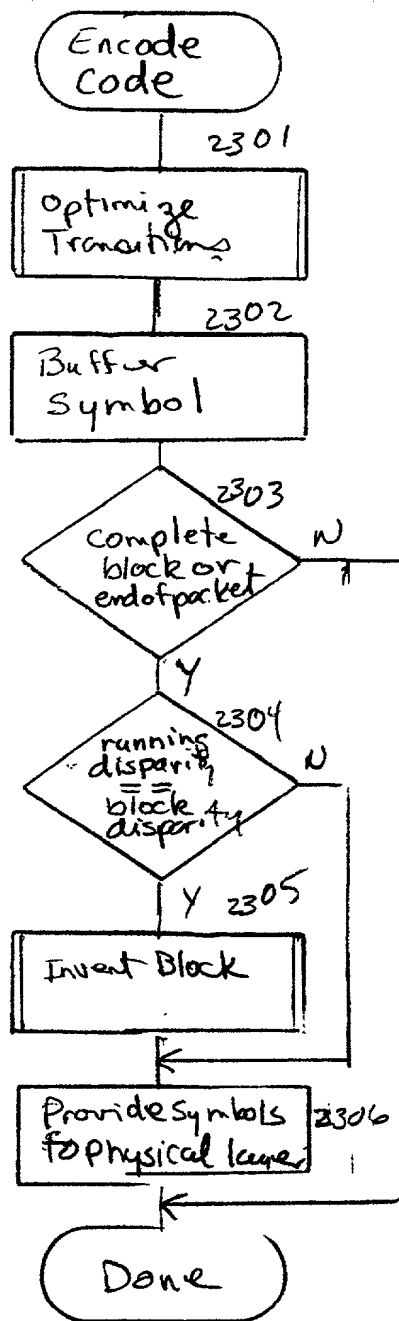


Fig 23

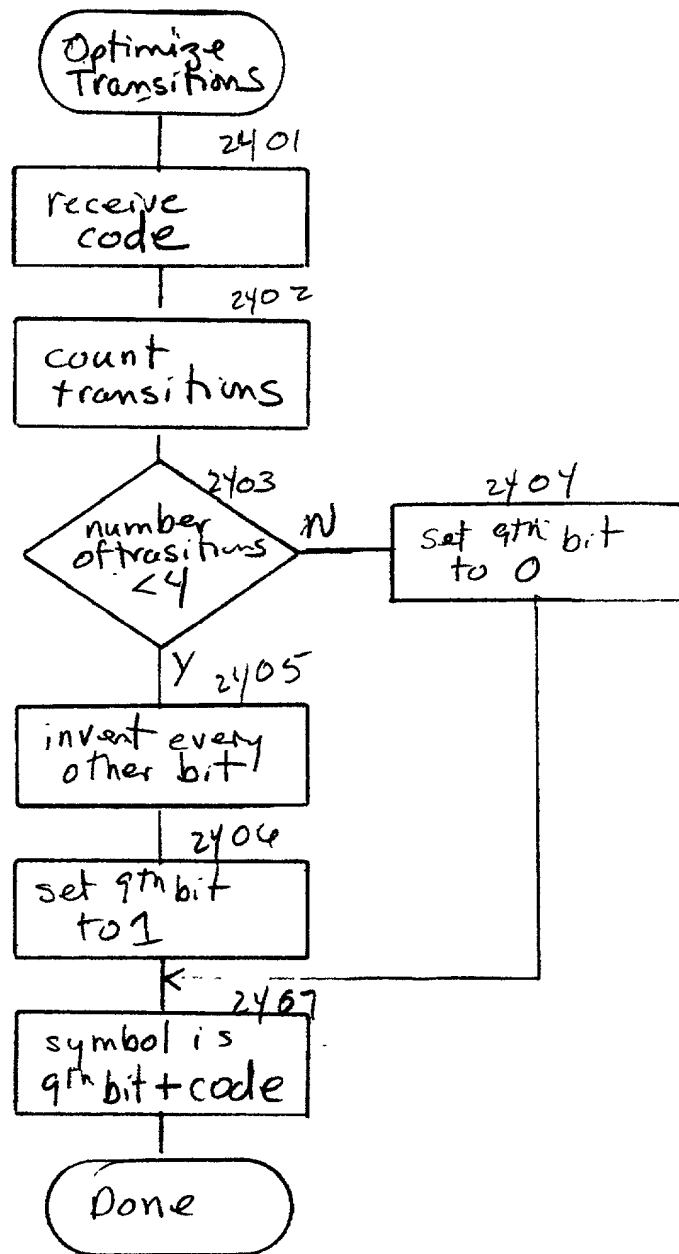


Fig 24

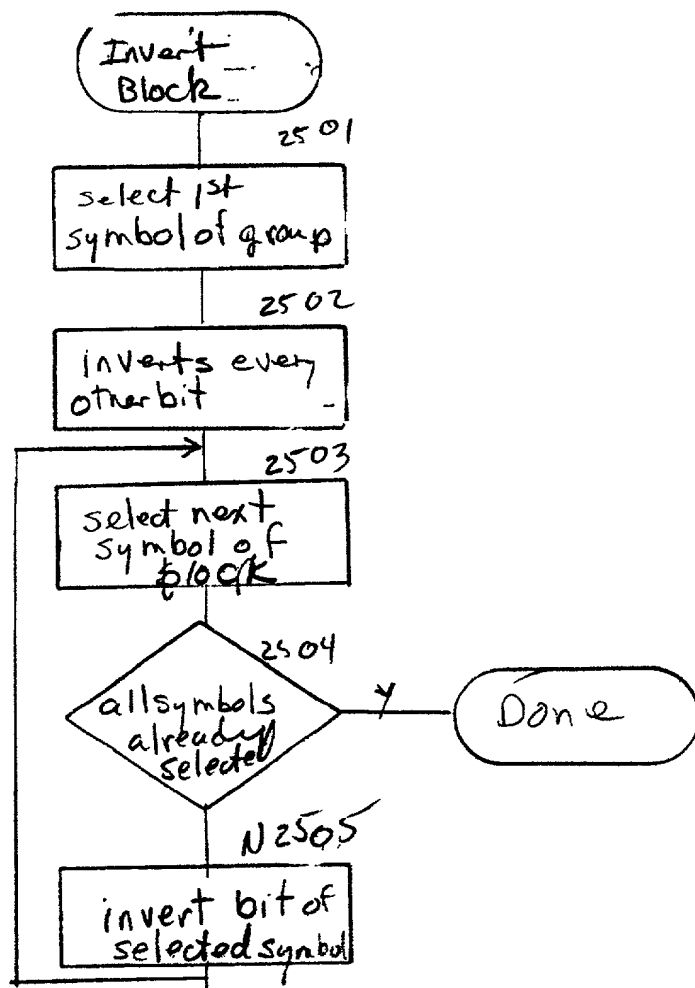


Fig 25

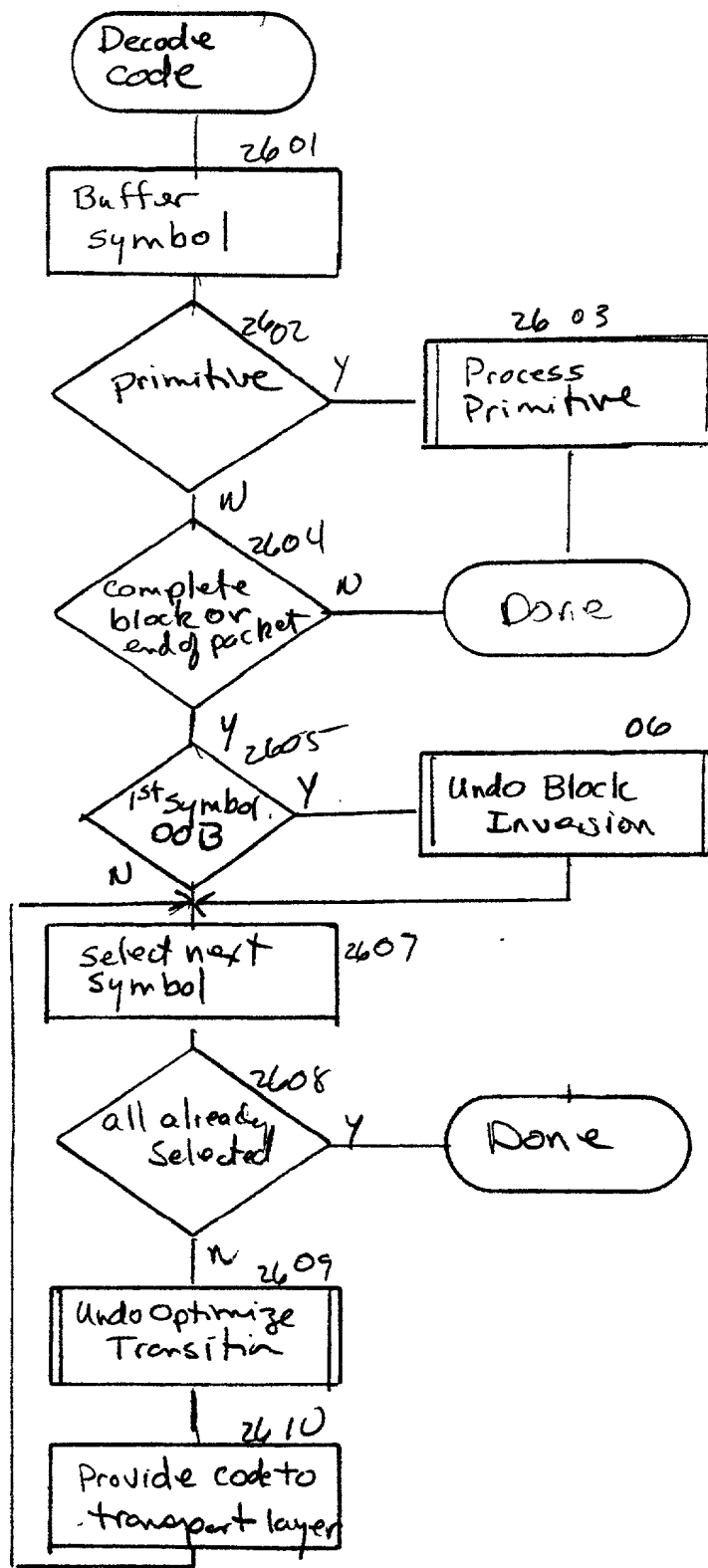


Fig 26

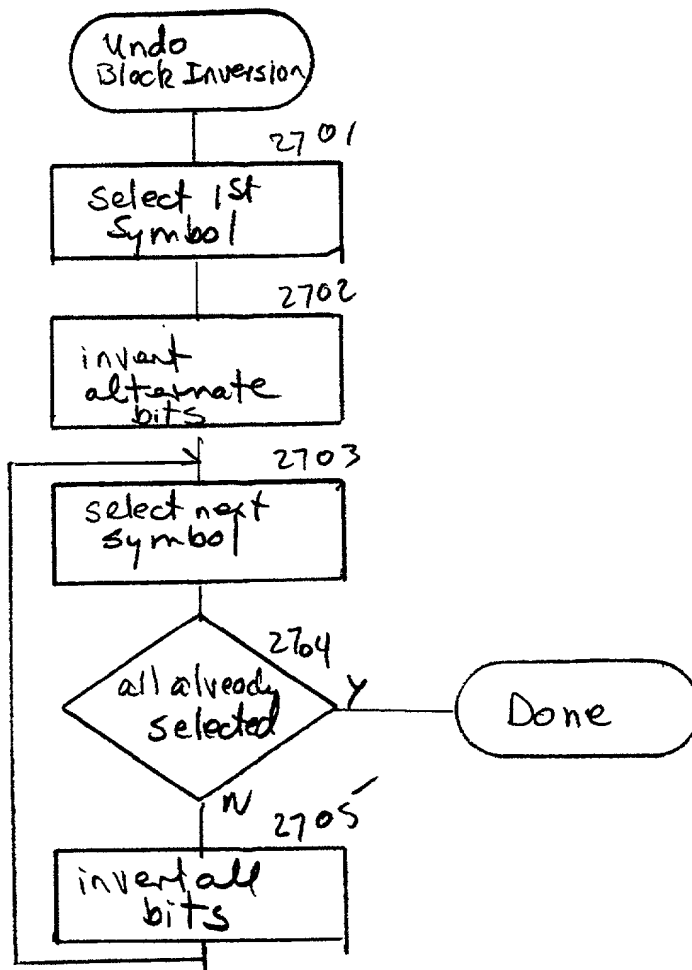


Fig 27

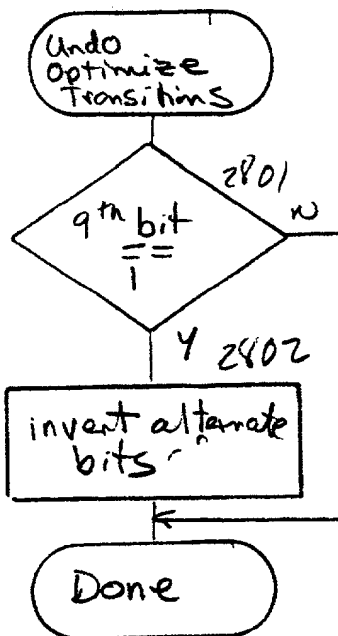


Fig 28

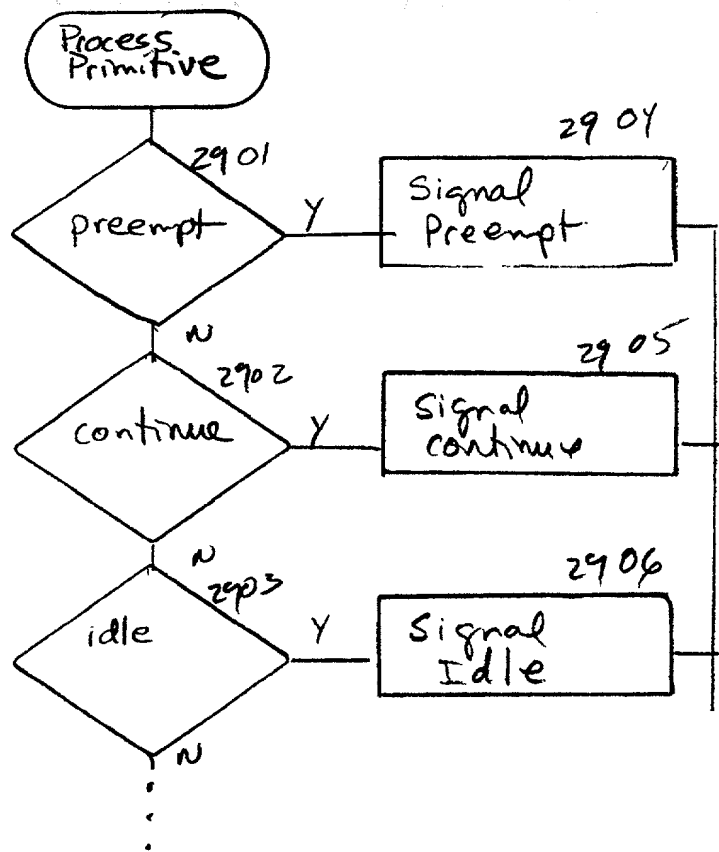


Fig 29

Multipoint Memory Device 3000

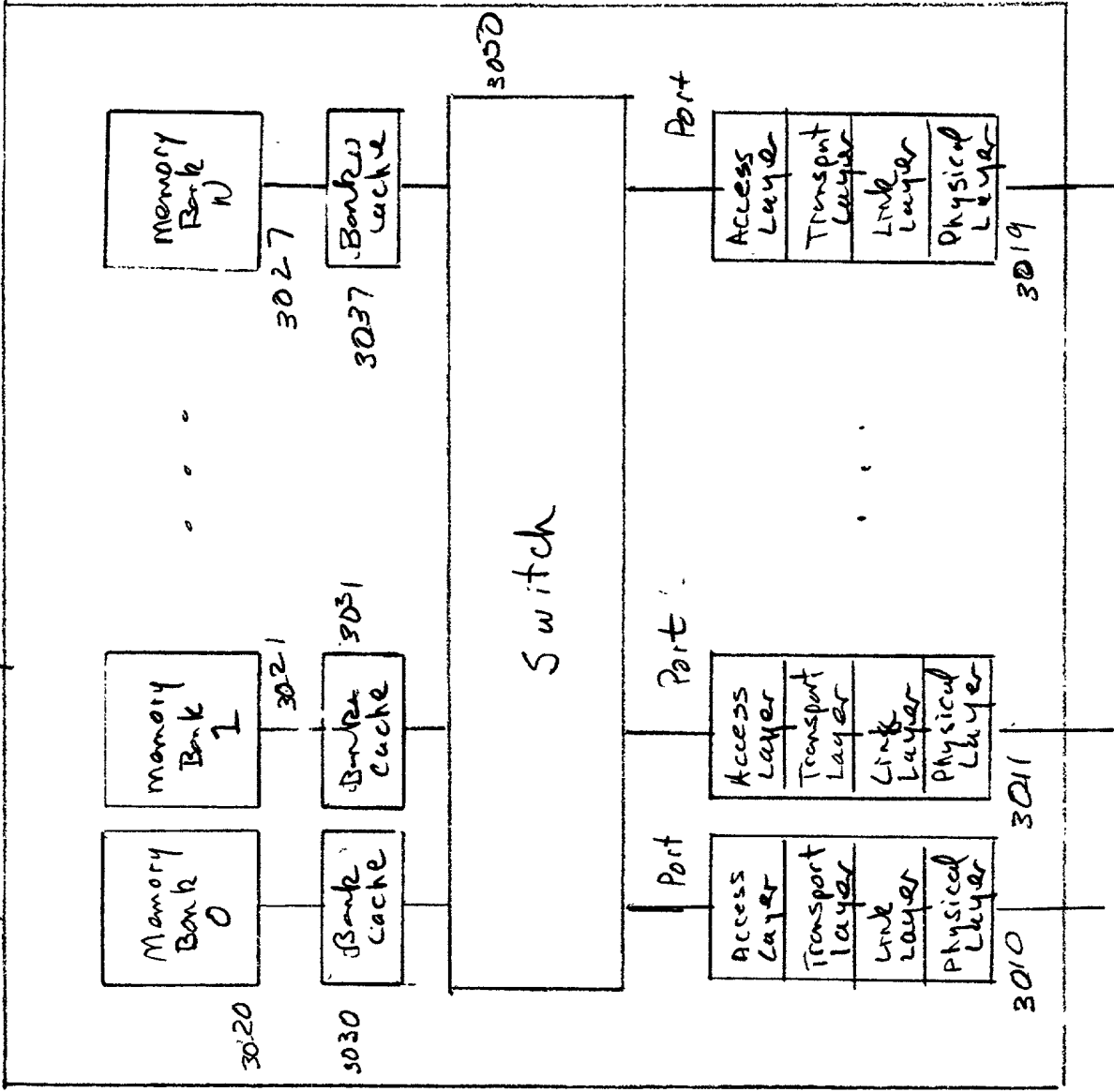


Fig 30

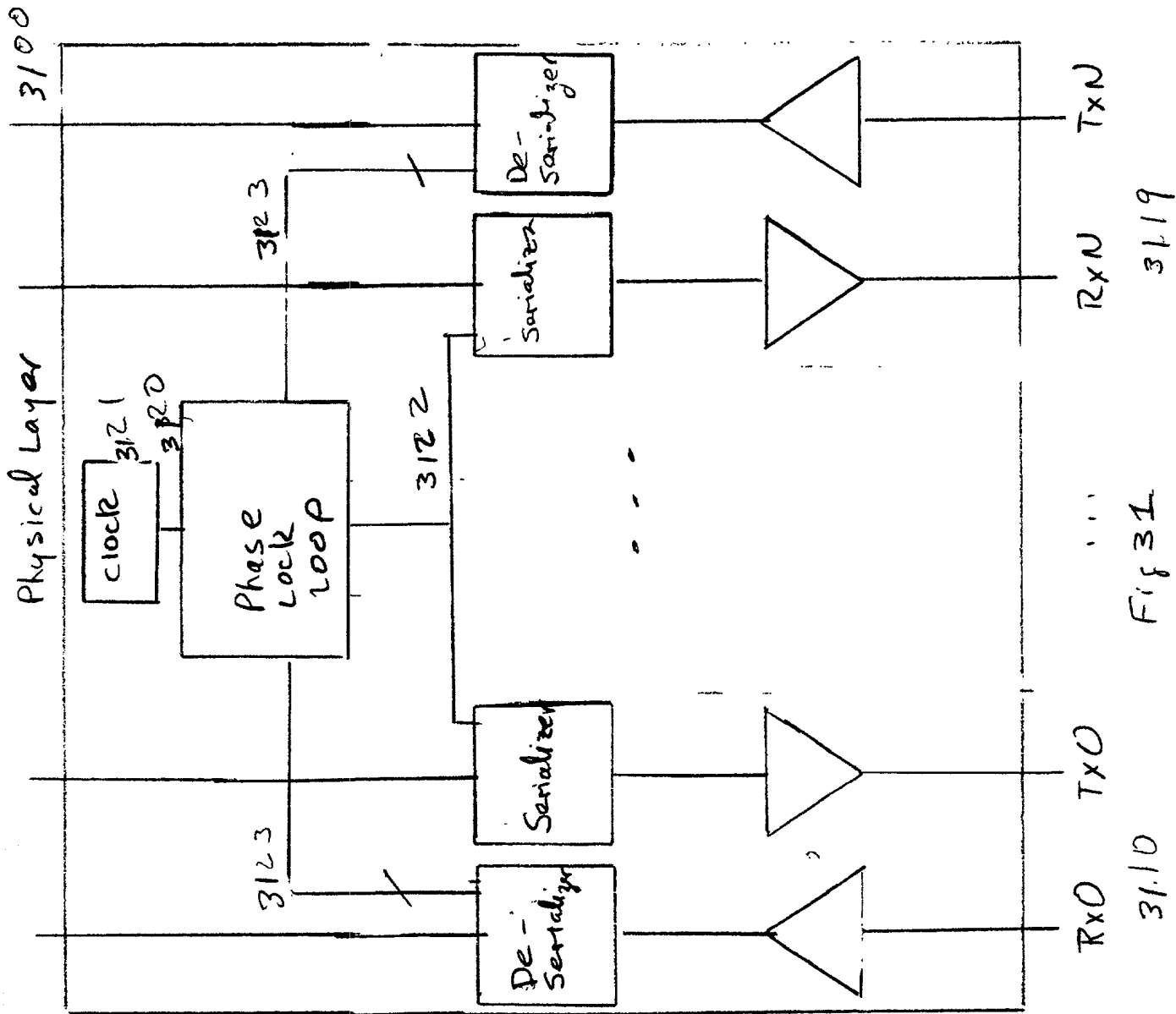


Fig 31

| Input Queue 3201 | | | Output Queue 3202 | | |
|------------------|-----|---------|-------------------|------|---------|
| Port | R/W | Address | Valid | Port | Data |
| 3 | R | 1000 | 1 | 3 | 11...0 |
| 4 | W | 4000 | 0 | | |
| 3 | W | 1000 | 0 | | |
| 3 | R | 2000 | 1 | 3 | 101...1 |
| | | | | ⋮ | |
| | | | | | |

Fig 32

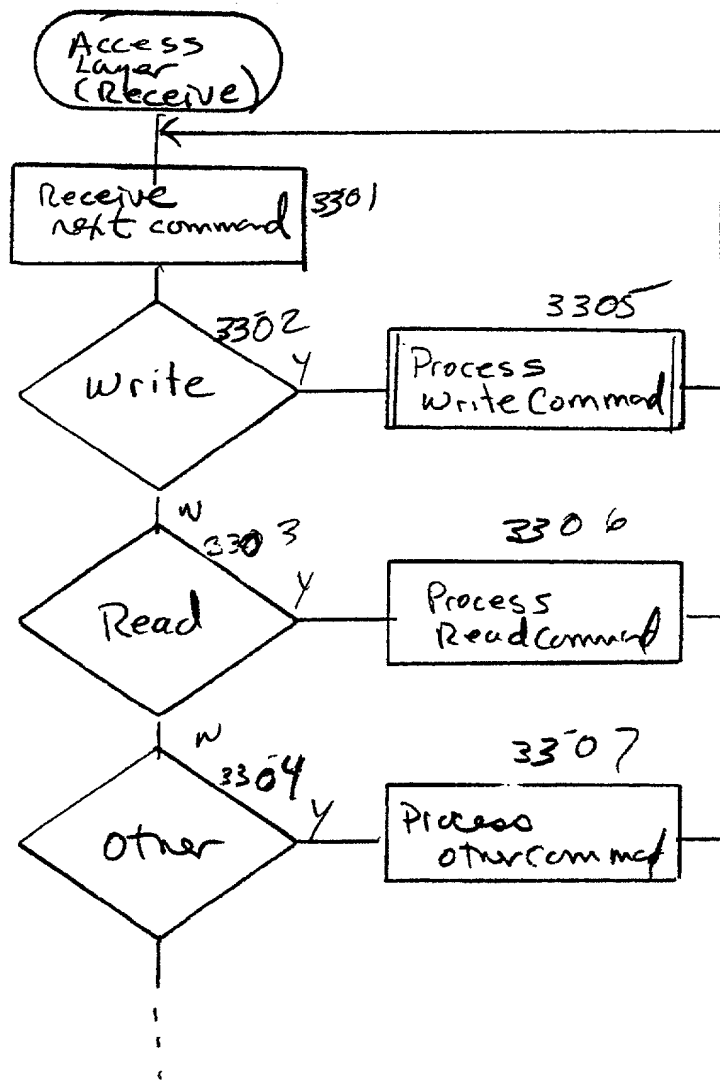


Fig 33

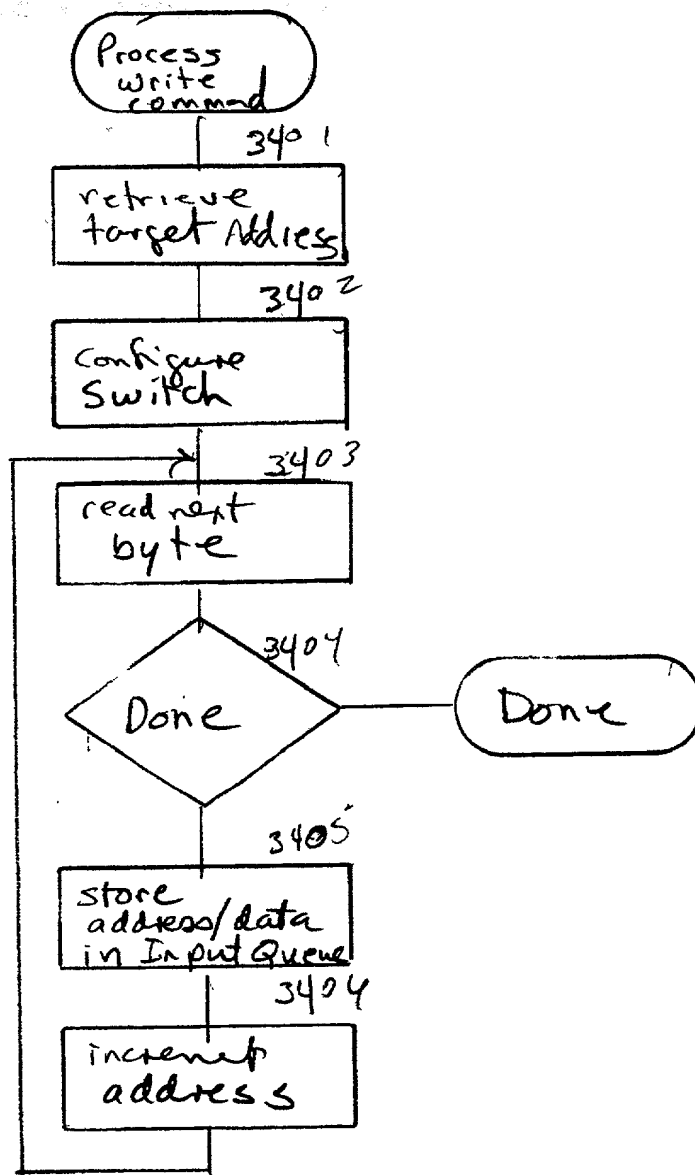


Fig 34

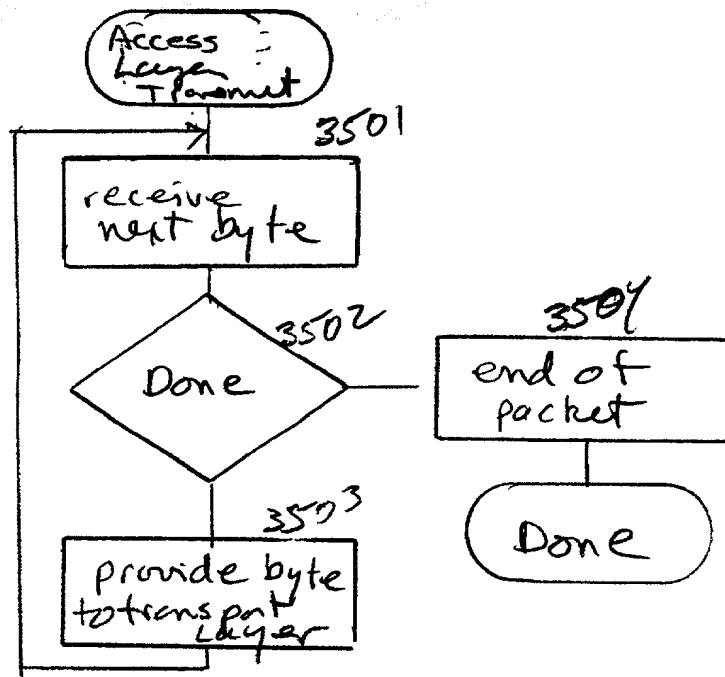


Fig 35

FOOTPRINT

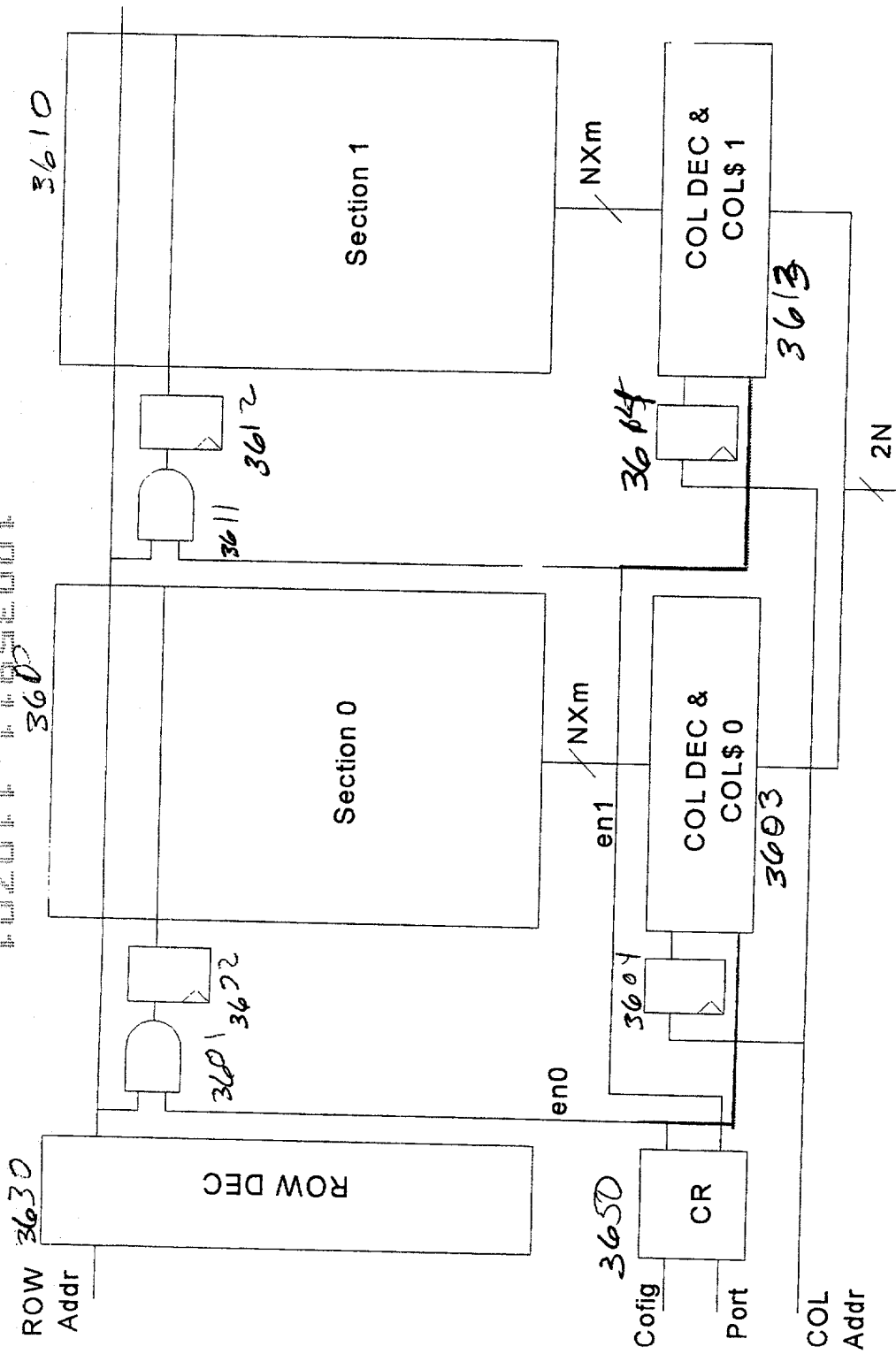
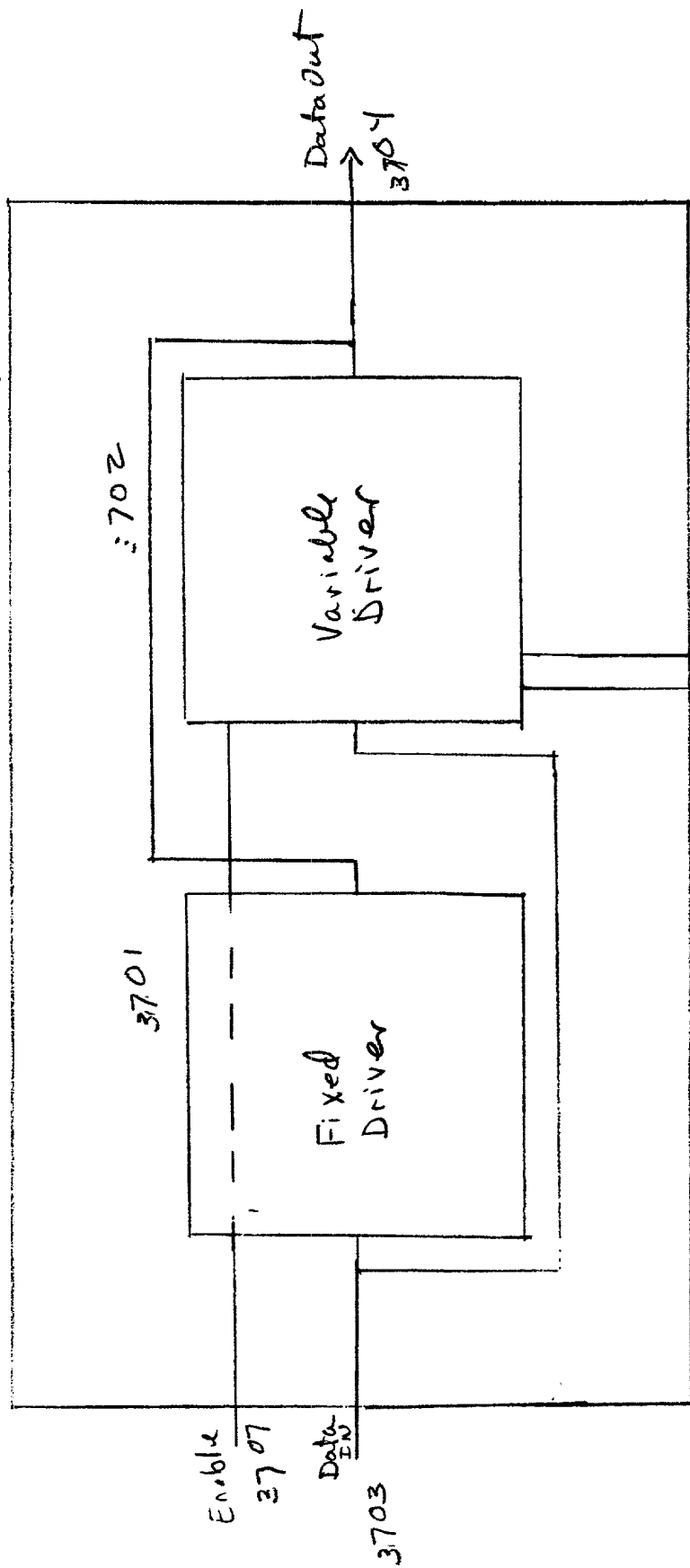


Fig 36

Line Driver 3700



Variable Driver

$$\begin{cases} RD^+ \wedge \overline{DataIn} = \text{pull down} \\ RD^- \wedge DataIn = \text{pull up} \end{cases}$$

Fig 37A

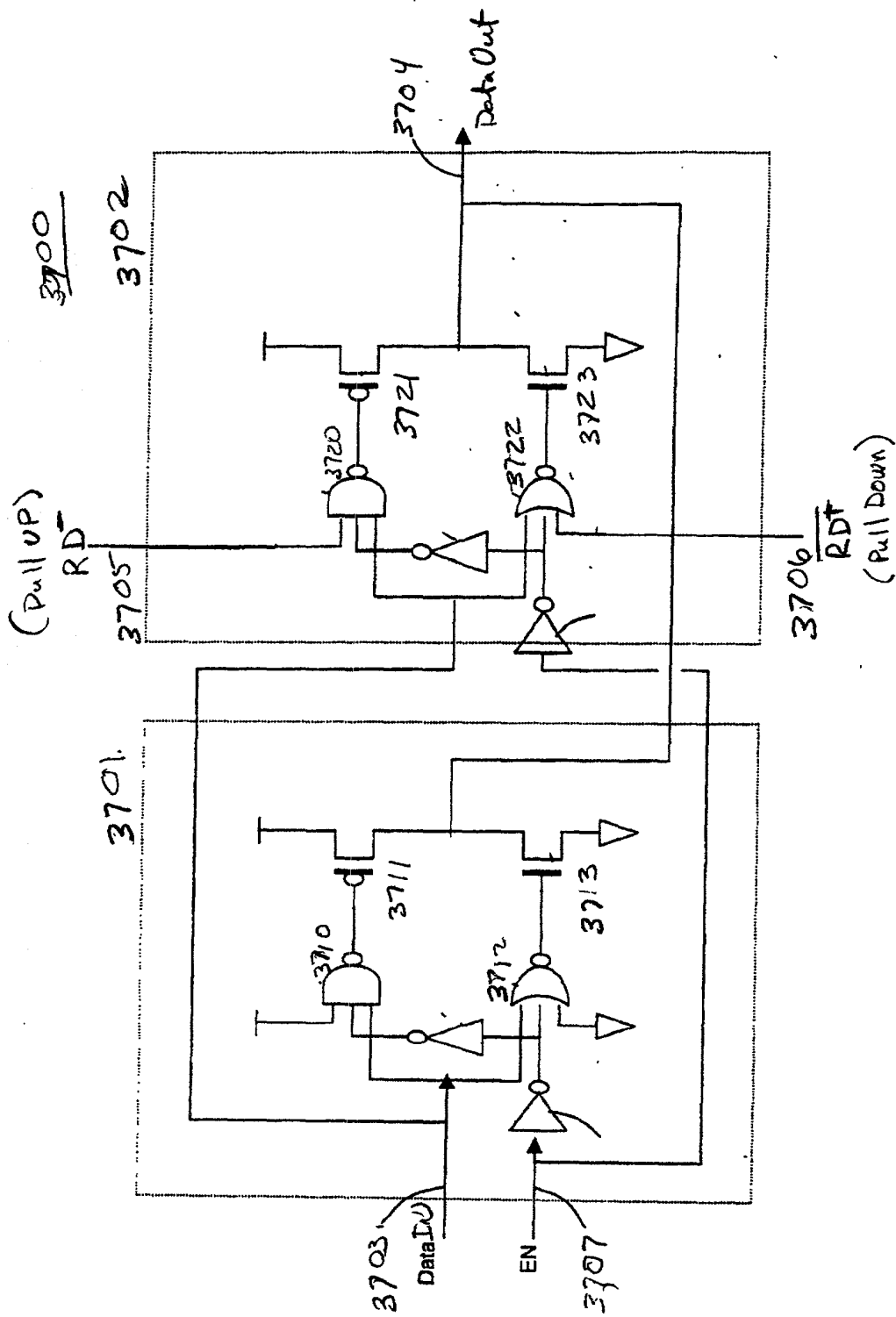


Fig 37B

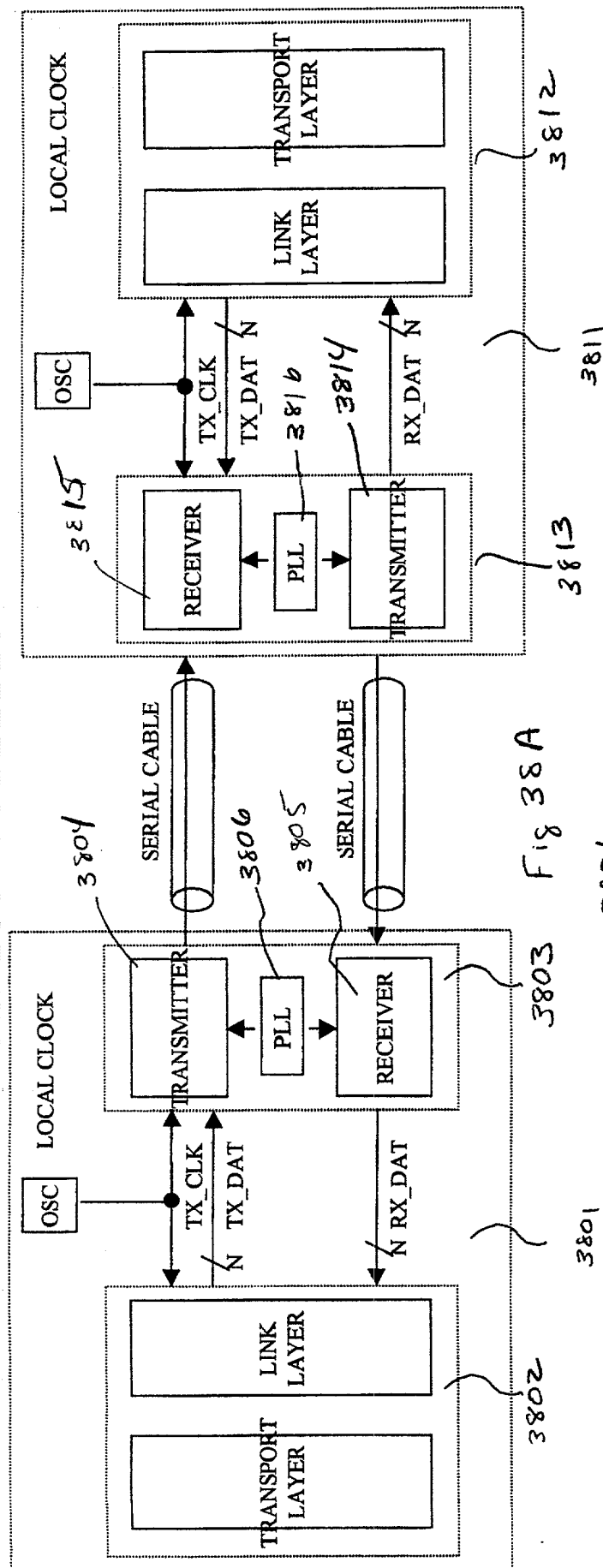


Fig 38A

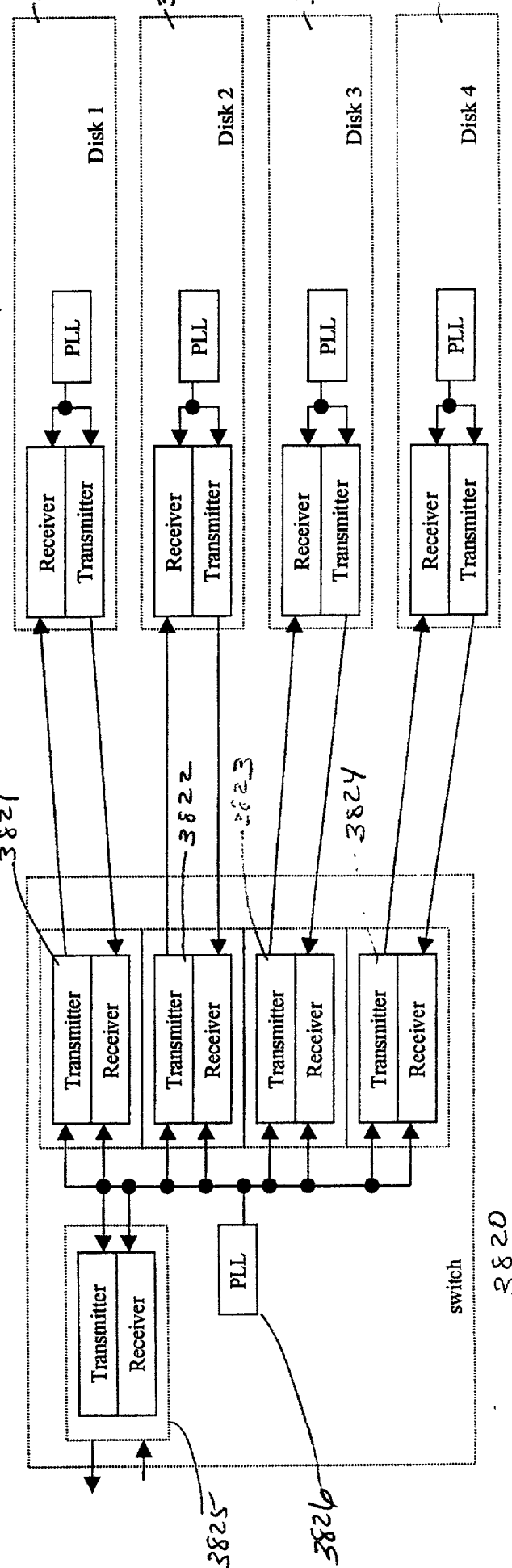


Fig 38B

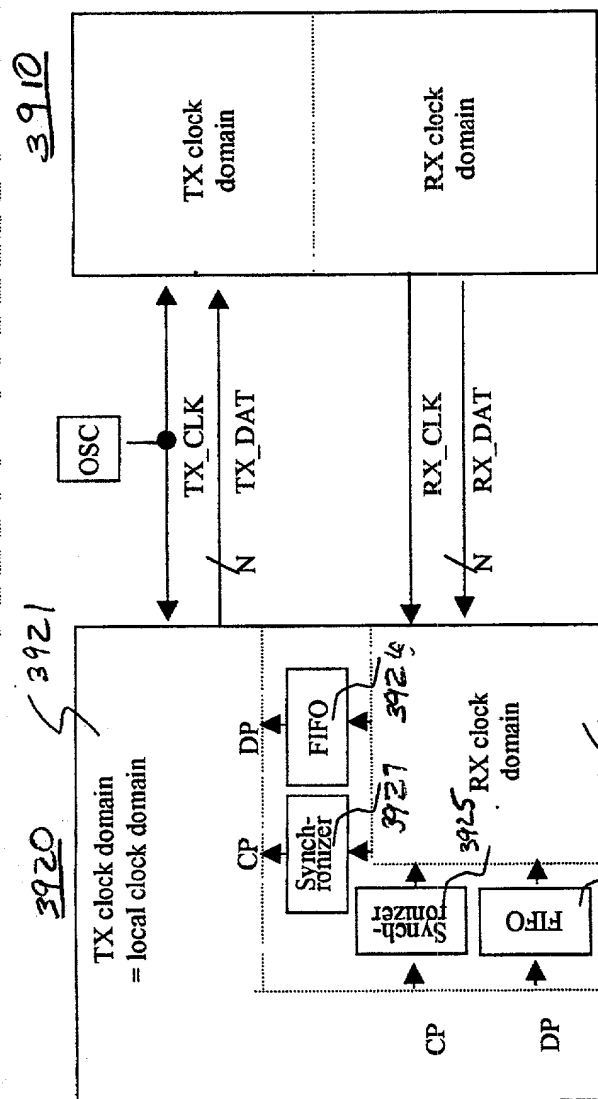


Fig 39A

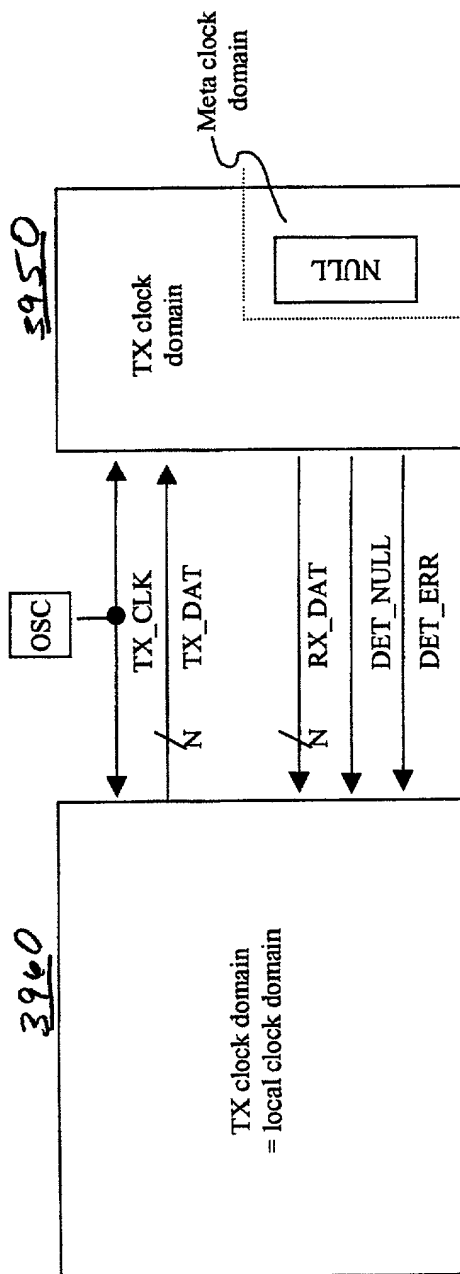


Fig 39B

FIG. 40

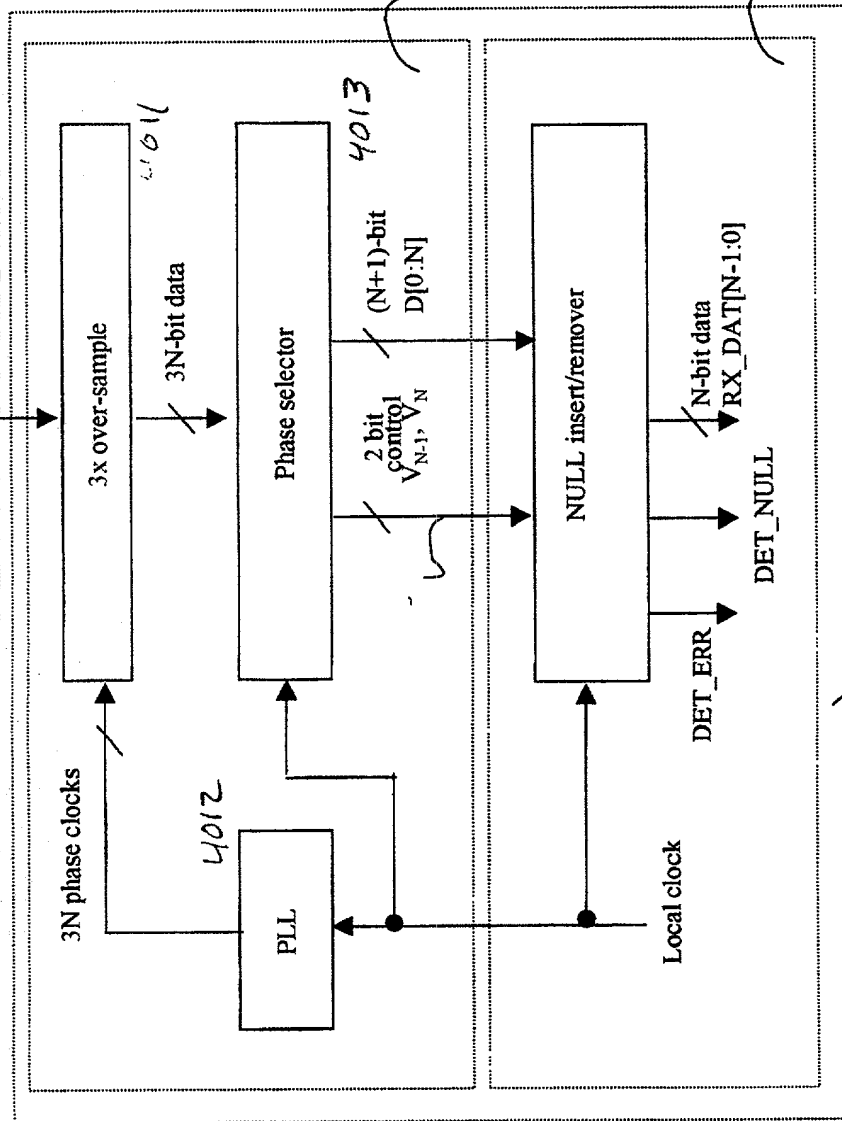


Fig 40

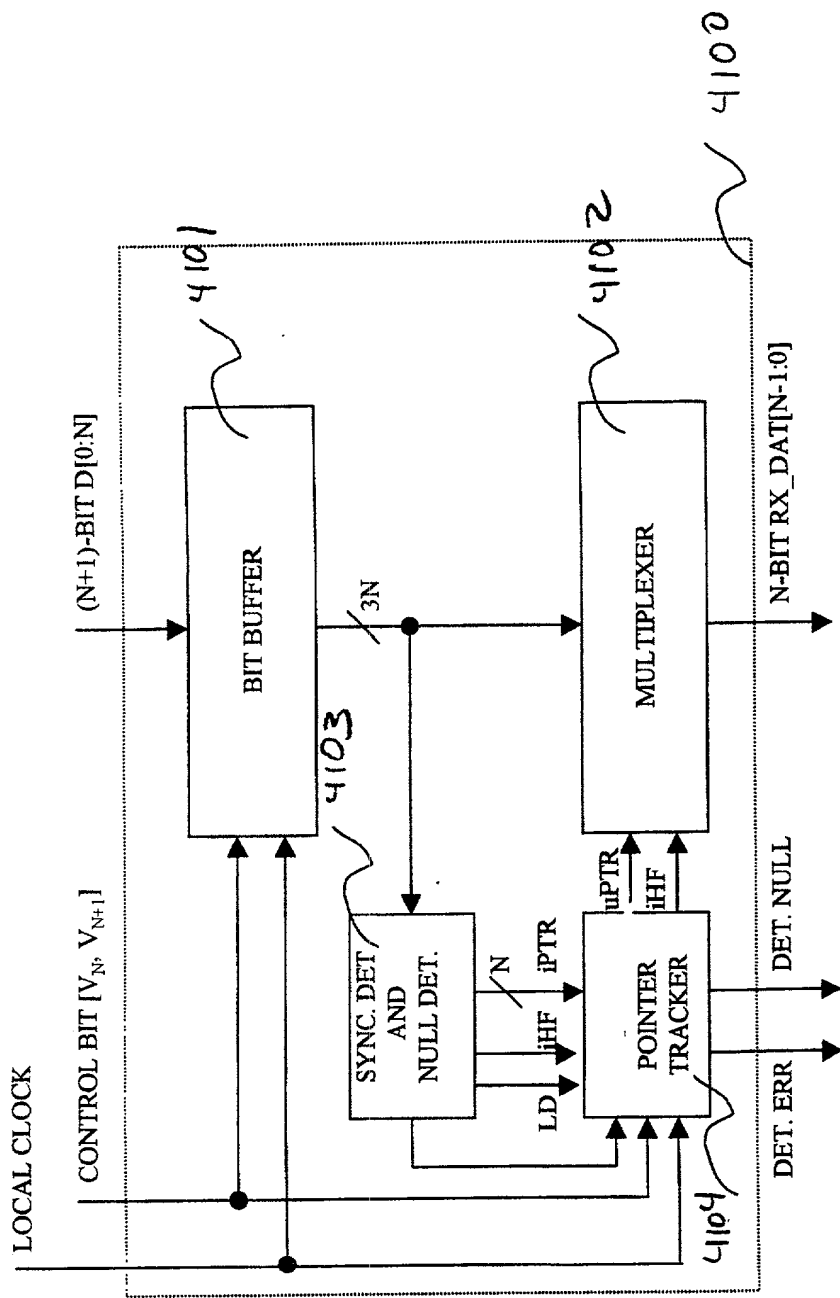


Fig 41

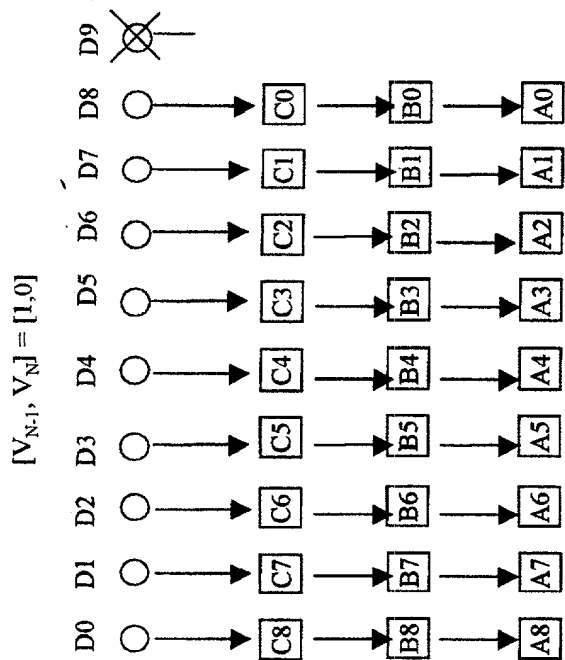


Fig 42A

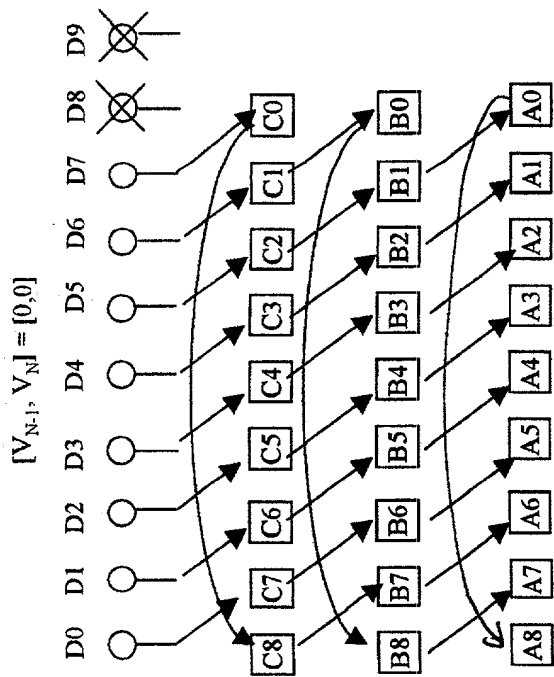


Fig 42B

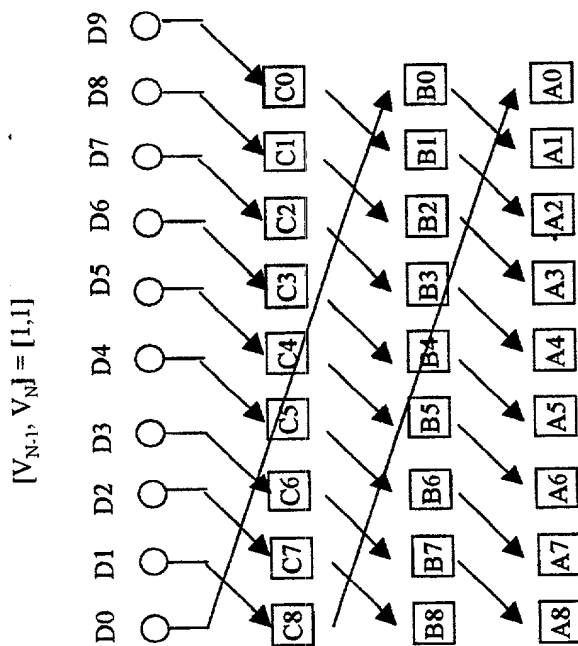


Fig 42c

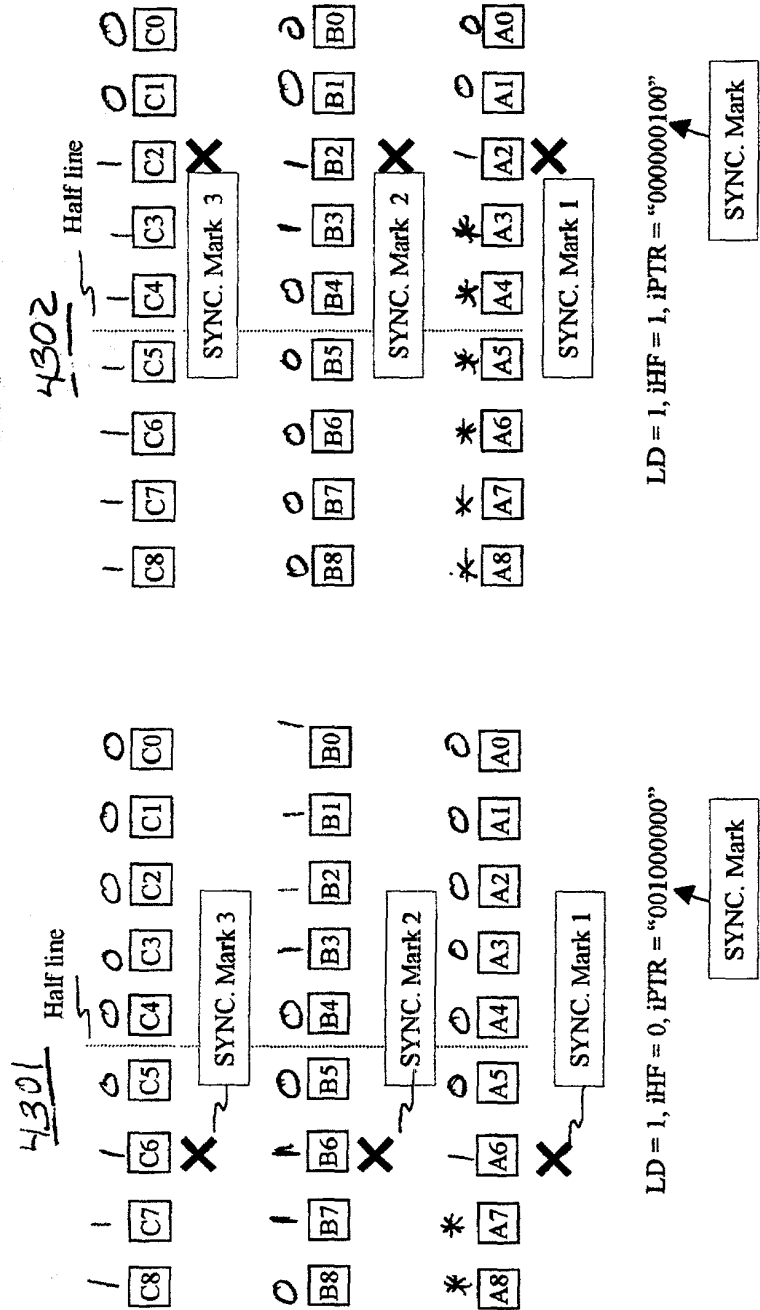
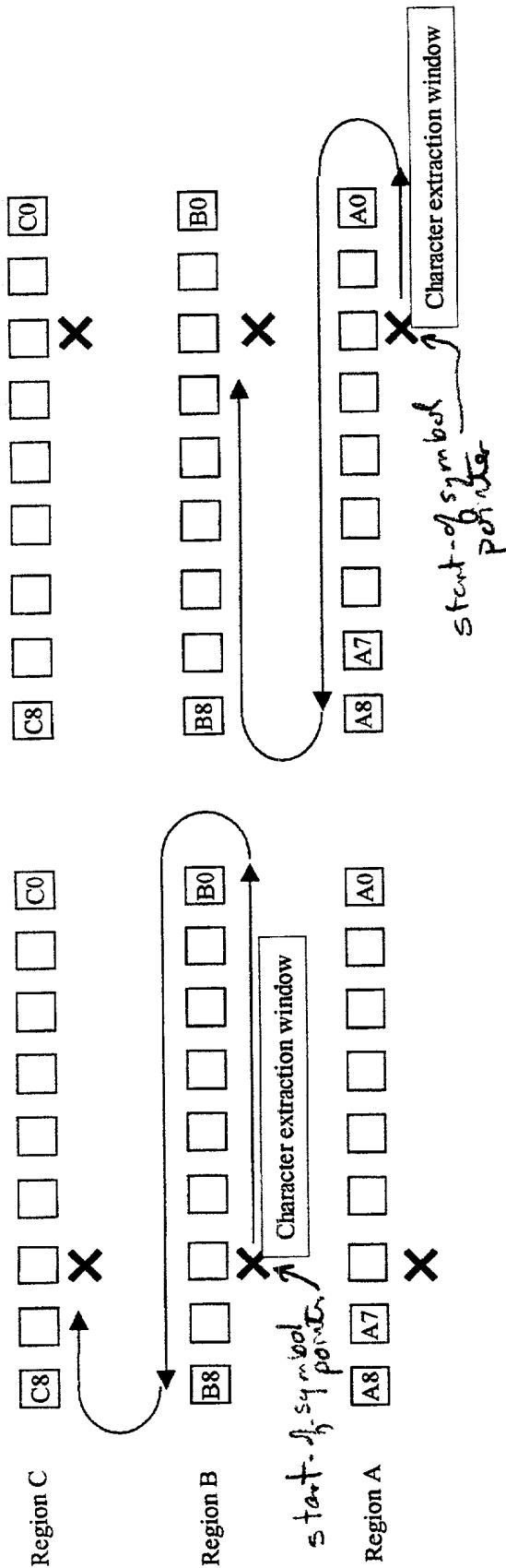


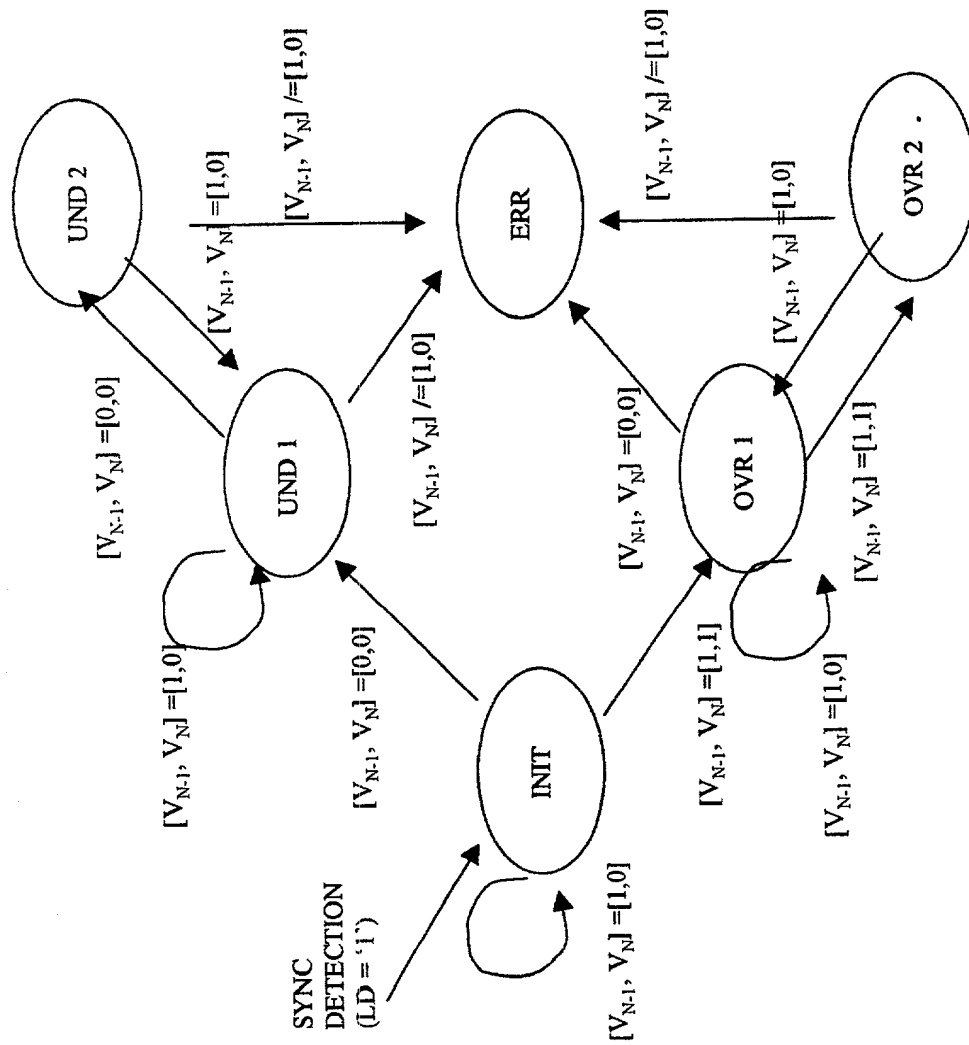
Fig. 43



LD = 1, iHF = 0, iPTR = "001000000"

LD = 1, iHF = 1, iPTR = "000000100"

Fig 44



F.8 45

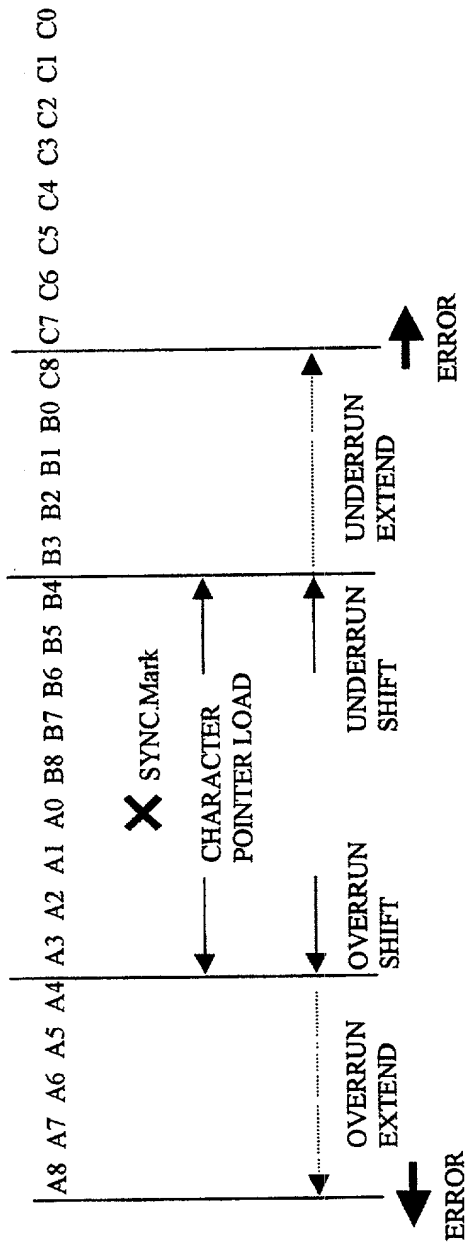


Fig 46

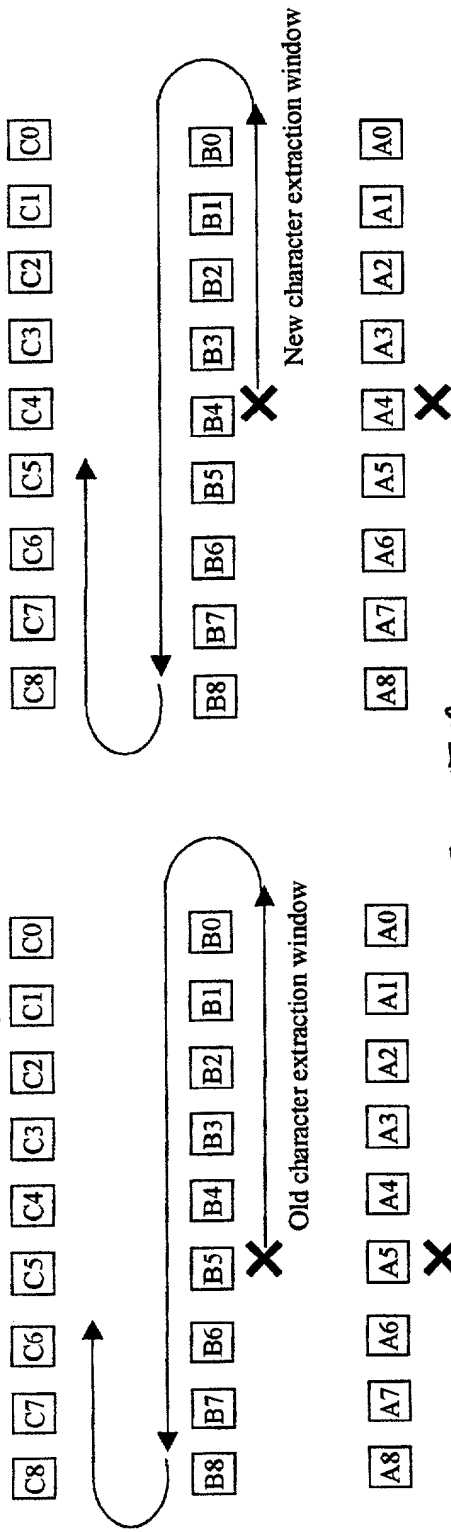


Fig 47A

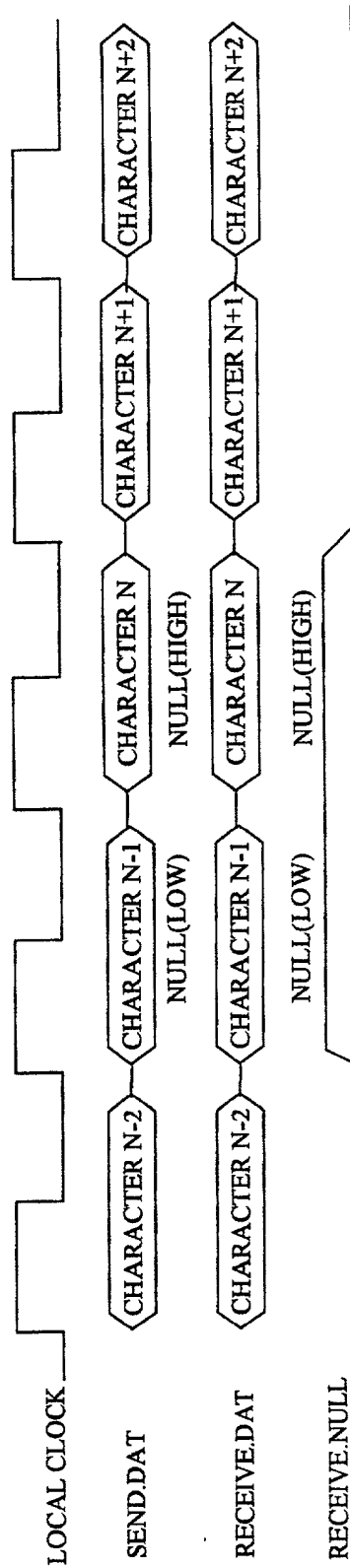


Fig 47B

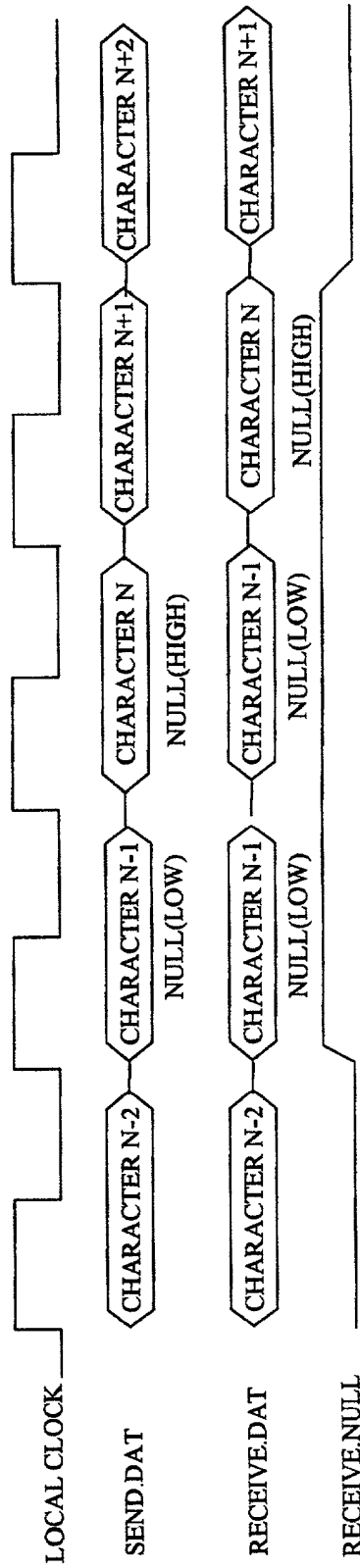
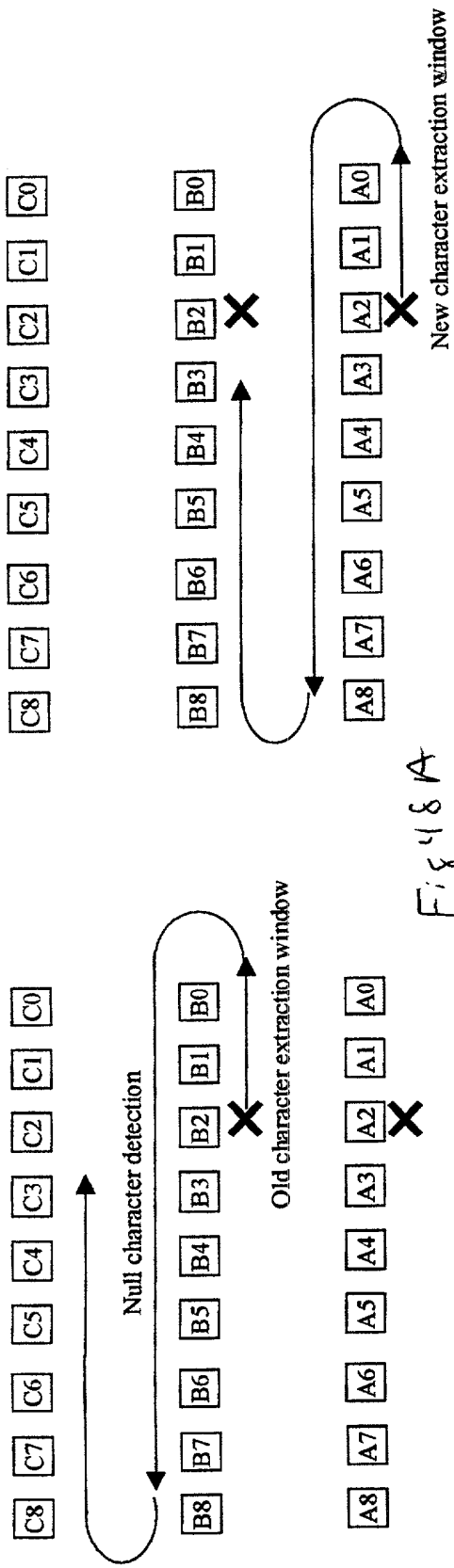


Fig 48B

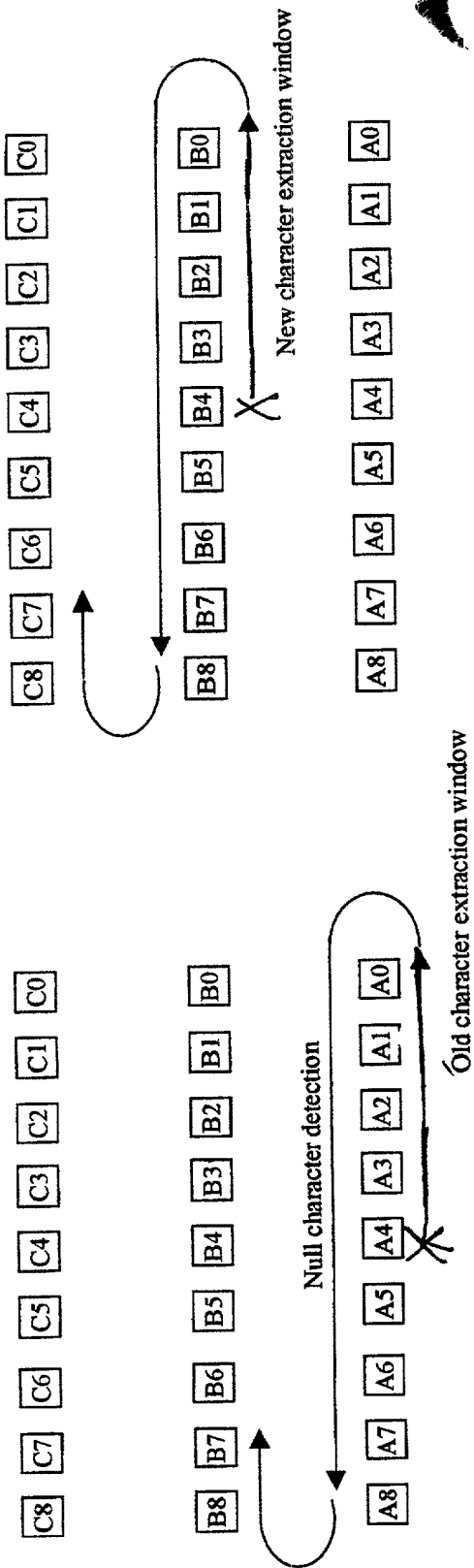


Fig. 49A

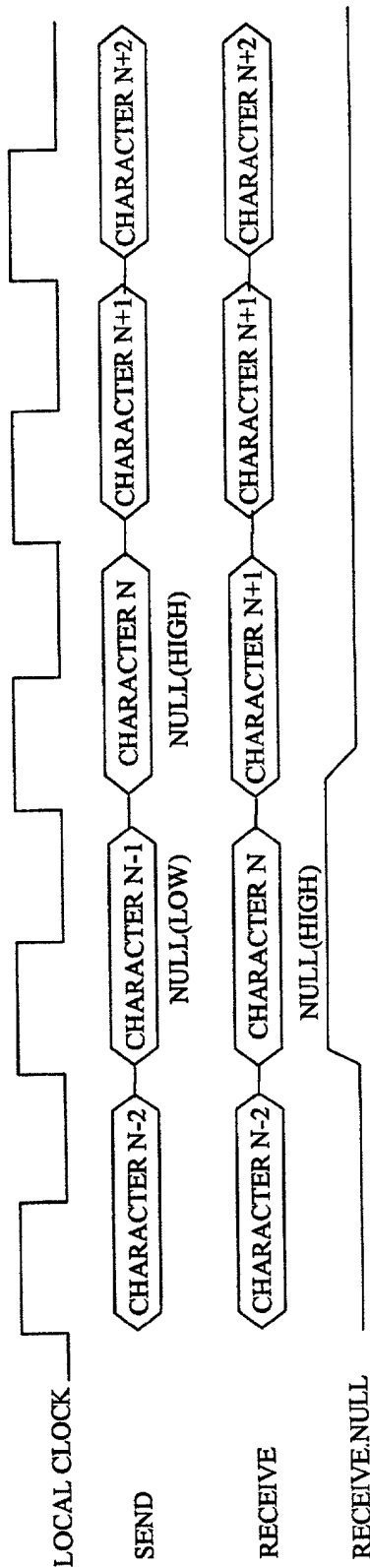


Fig 49B